

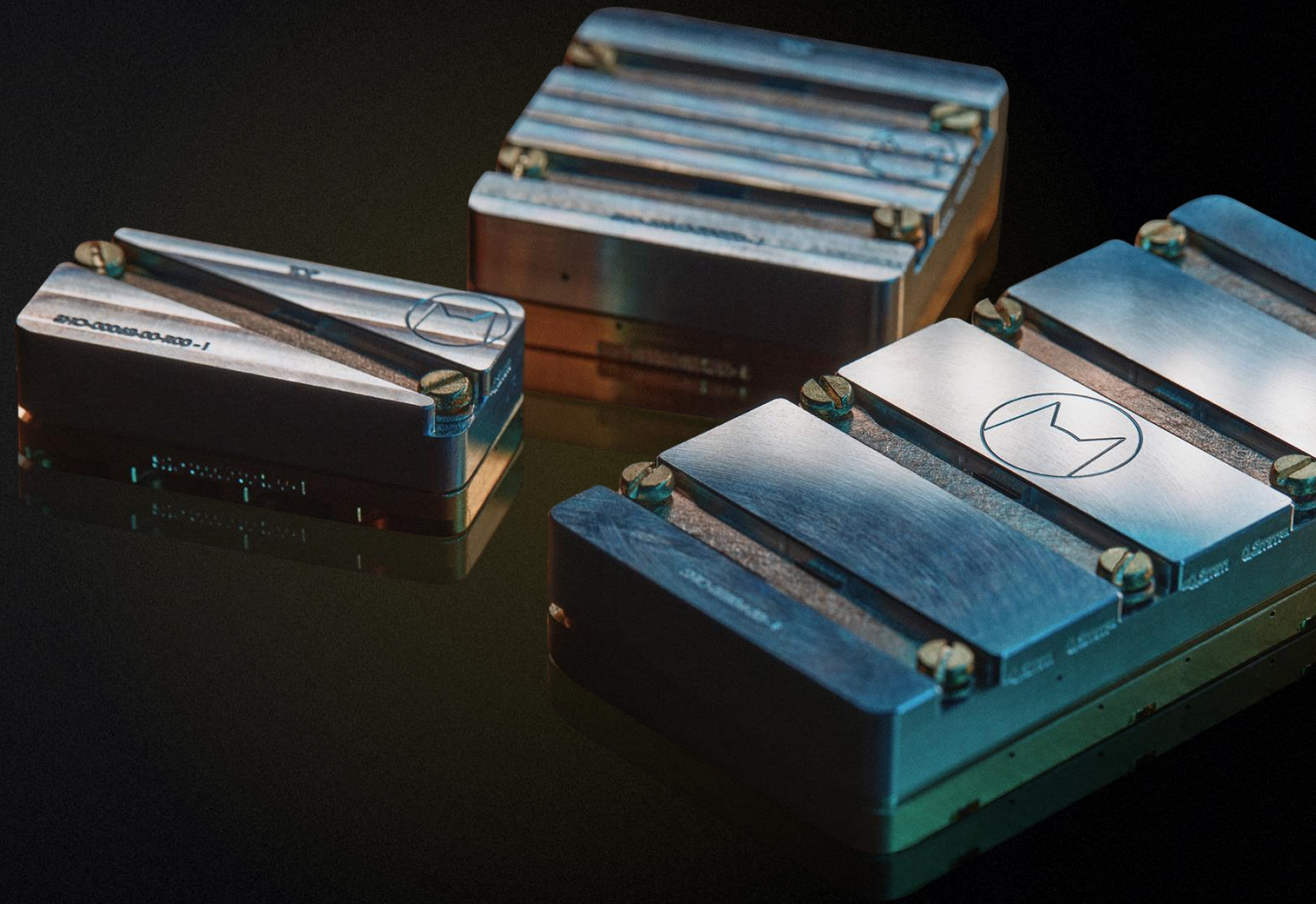
PQCSA

ADVANCEMENT IN QUANTUM COMPUTERS AND SHOR'S ALGORITHM

PQSCA Workshop

Dahmun Goudarzi

MAY 19TH 2026





Alice & Bob

On the highway to FTQC

Théau Peronnin

CO-FOUNDER & CEO

Raphaël Lescanne

CO-FOUNDER & CTO



2020 Founding date

2 Offices
Paris / Boston

190+ People
40+ PhDs,
90+ R&D

€130m Raised to date

We are a **quantum processor** designer, focused on **FTQC**

We design the **quantum chip** & the **firmware** that drives it

We leverage a unique technology: the **superconducting cat qubit**

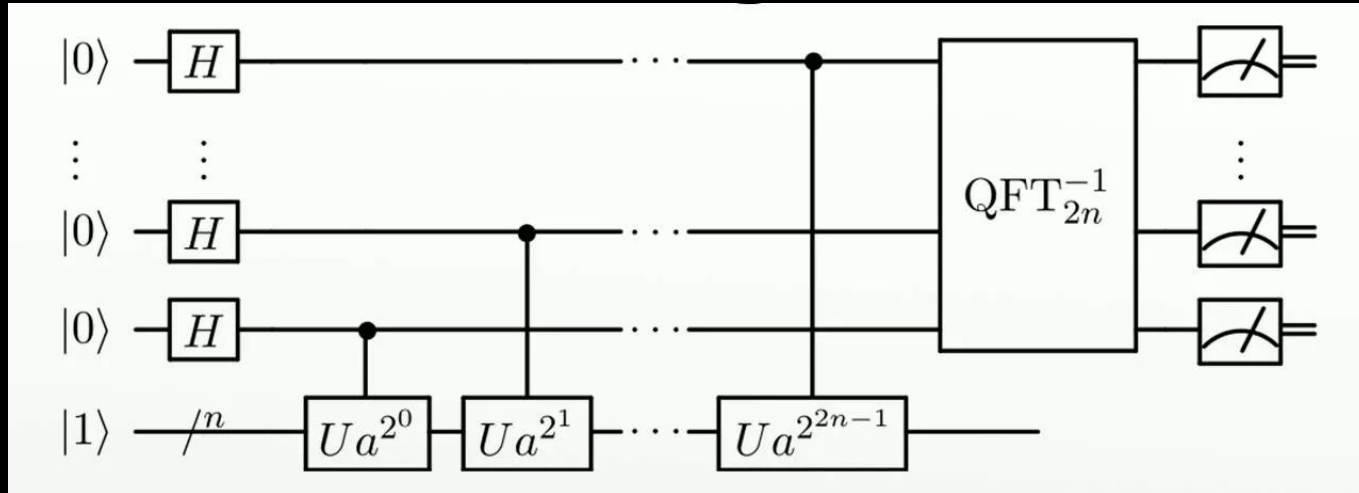


“Nature isn't classical, dammit, and if you want to make a simulation of nature, you'd better make it quantum mechanical, and by golly it's a wonderful problem, because it doesn't look so easy”

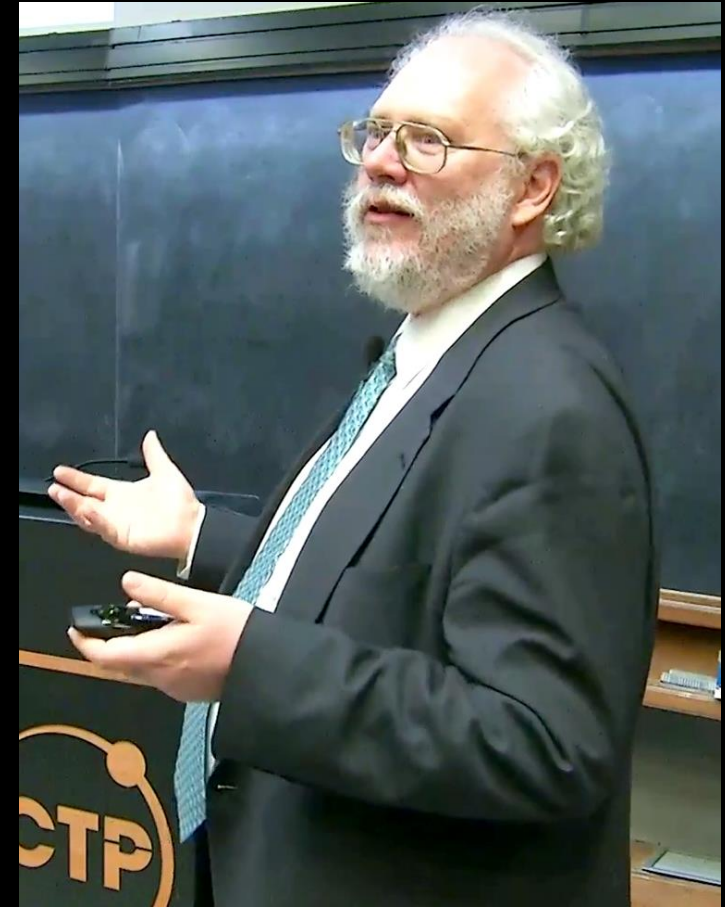
Richard Feynman



1981



Shor's algorithm



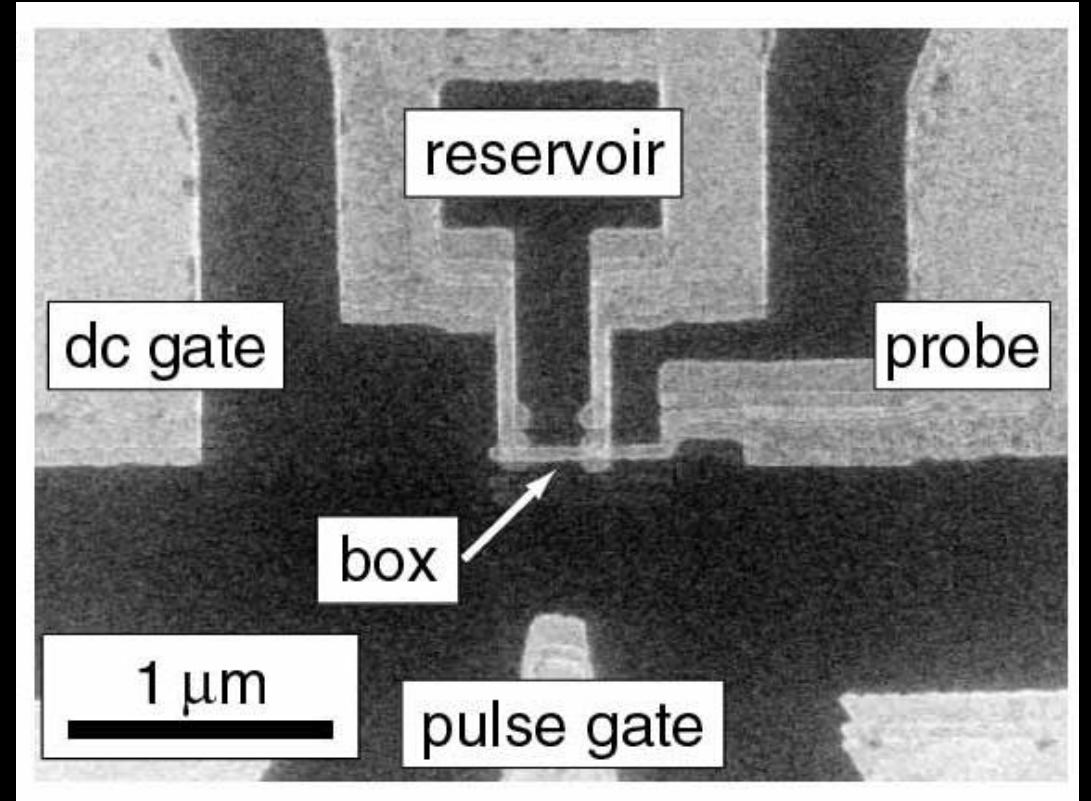
1981

1994





The first qubits



Coherent control of macroscopic quantum states in a single-Cooper-pair box
Y. Nakamura et al., 1999, <https://www.nature.com/articles/19718>

1981

1994

1999





The first quantum computers in the cloud

The screenshot shows the IBM Quantum Playground interface. On the left, a 'time to try' timer is set to 70 minutes, with a 'request chip' button and the text 'Running in Virtual Chip'. The main area displays a quantum circuit with four qubits (Q1, Q2, Q3, Q4). Q1 has an $X_{\pi/2}$ gate, Q2 has a $Y_{\pi/2}$ gate, and Q3 has an $X_{\pi/2}$ gate. A CNOT gate is applied between Q1 and Q3. A measurement gate is on Q3. A 'Run' button is on the right. Below the circuit is a list of examples: Rabi, Rotary, T1_measurement, Ramsey, Hanh Echo, Calibrate Single, Benchmark Single, and Calibrate Readout out. On the right side of the interface, there is a graph showing a signal decay curve with the text $T_1 = 40206.907050 \text{ ns}$.



1981

1994

1999

2016



The first claim of quantum supremacy



Google

1981

1994

1999

2016

2019





Where are we today?





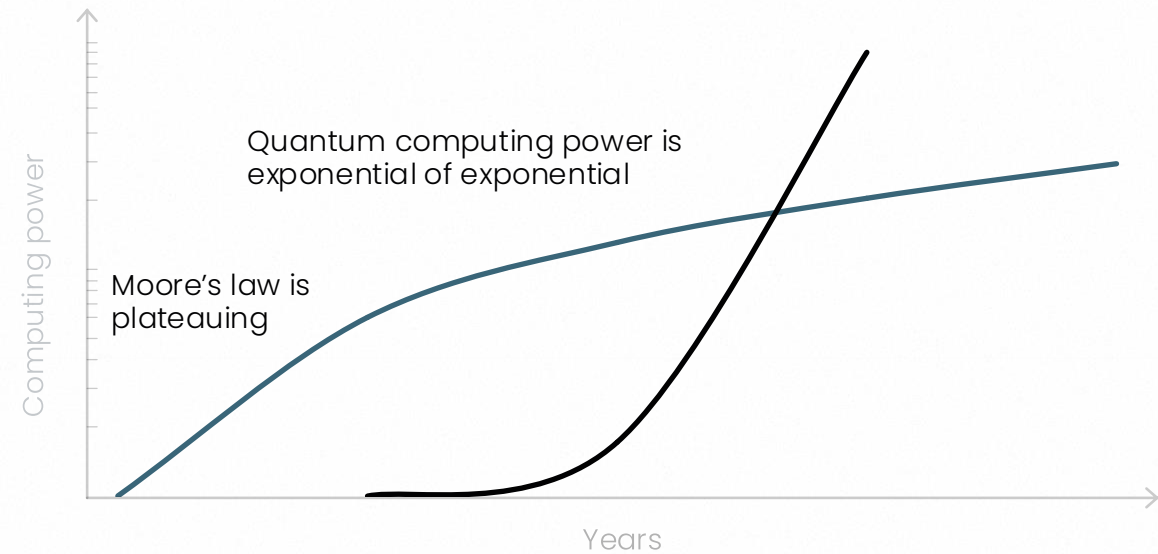
Forget about quantum: the goal is exponential increase in computing power to create value

Quantum is the engine of the next
economic breakthrough

→ Source

1. [McKinsey](#)

Quantum computing is the only way to
generate exponential increase in
computing power

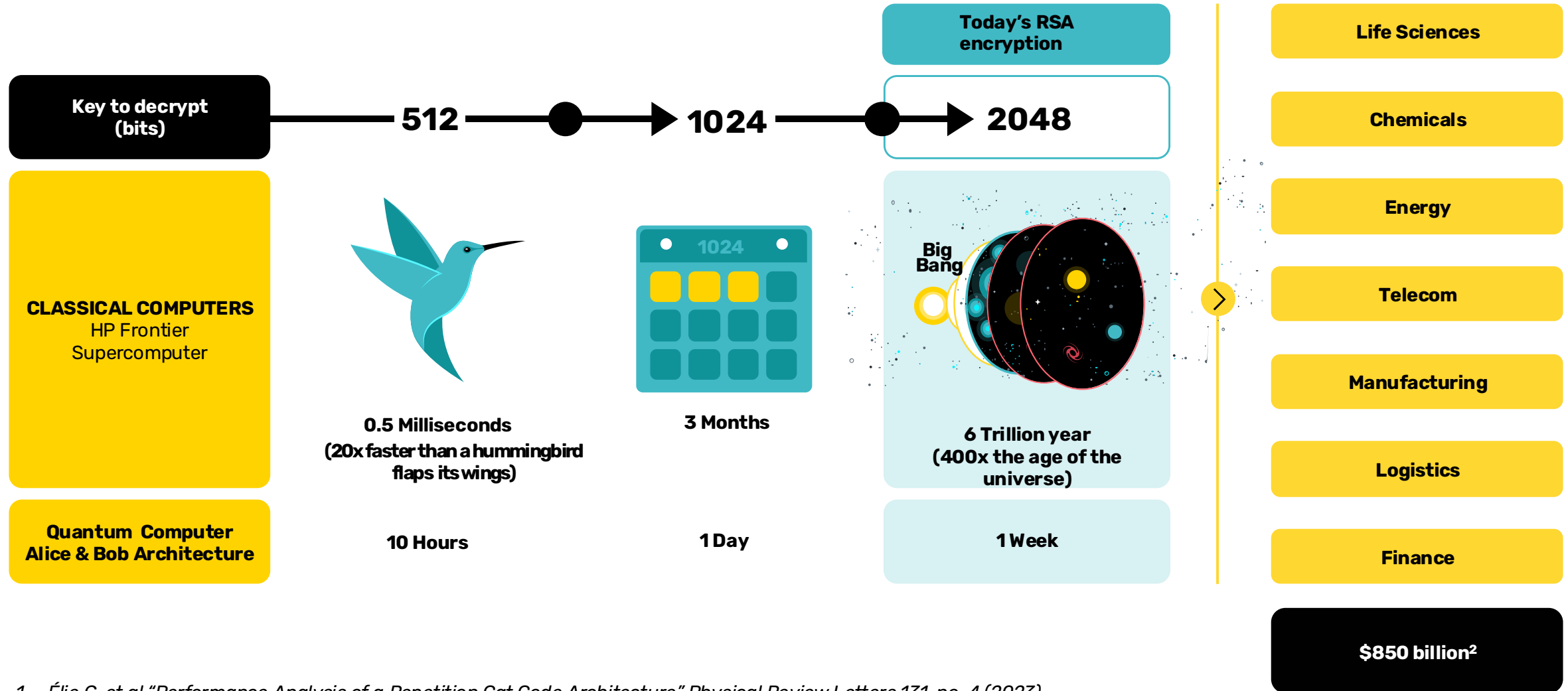


+1 qubit

= x2
computing power



Quantum Computers Expand the Set of Problems we can Solve, Unlocking a Technological Revolution



1. Élie G. et al. "Performance Analysis of a Repetition Cat Code Architecture" *Physical Review Letters* 131, no. 4 (2023).
 2. Boston Consulting Group, 2024



Quantum computing is NOT always faster

We compare the peak performance of a single classical chip that can be manufactured today (like an NVIDIA A100 GPU, or an ASIC with a similar number of transistors) with a future quantum computer with 10,000 error-corrected logical qubits, 10 μ s gate time for logical operations and all-to-all connectivity. We consider an estimate of the I/O bandwidth (namely the number of operations per second) and three types of operations: logical binary operations, 16-bit floating point, 32-bit integer or fixed-point arithmetic multiply add operations.

	GPU	ASIC	Future Quantum
I/O Bandwidth	10,000 Gbit/s	10,000 G/s	1 Gbit/s
Operation throughput			
16-bit floating point	195 Top/s	550 Top/s	10.5 kop/s
32-bit integer	9.75 Top/s	215 Top/s	0.83 kop/s
binary (Boolean logical)	4,992 Top/s	77,000 Top/s	235 kop/s





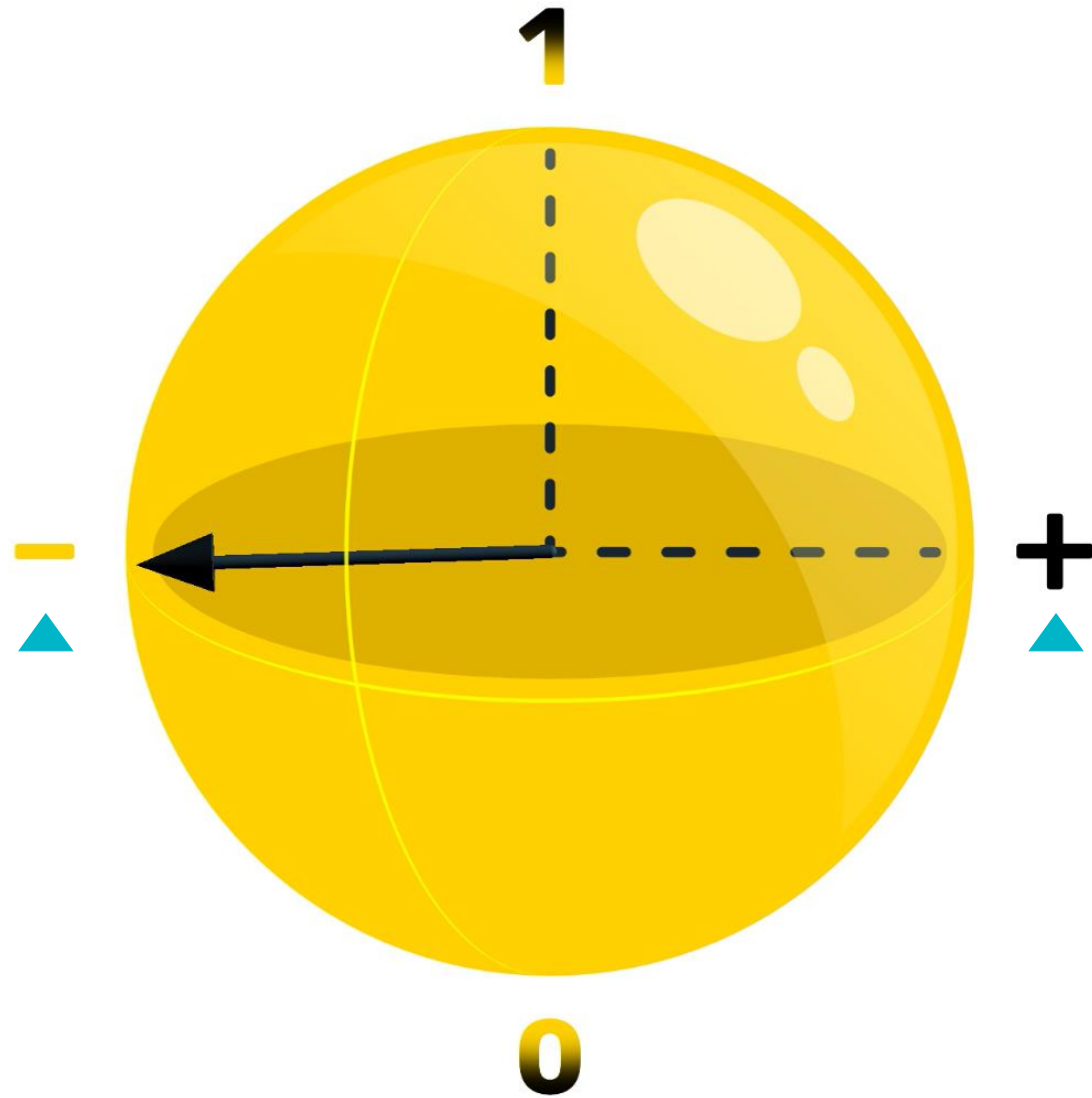
Bit





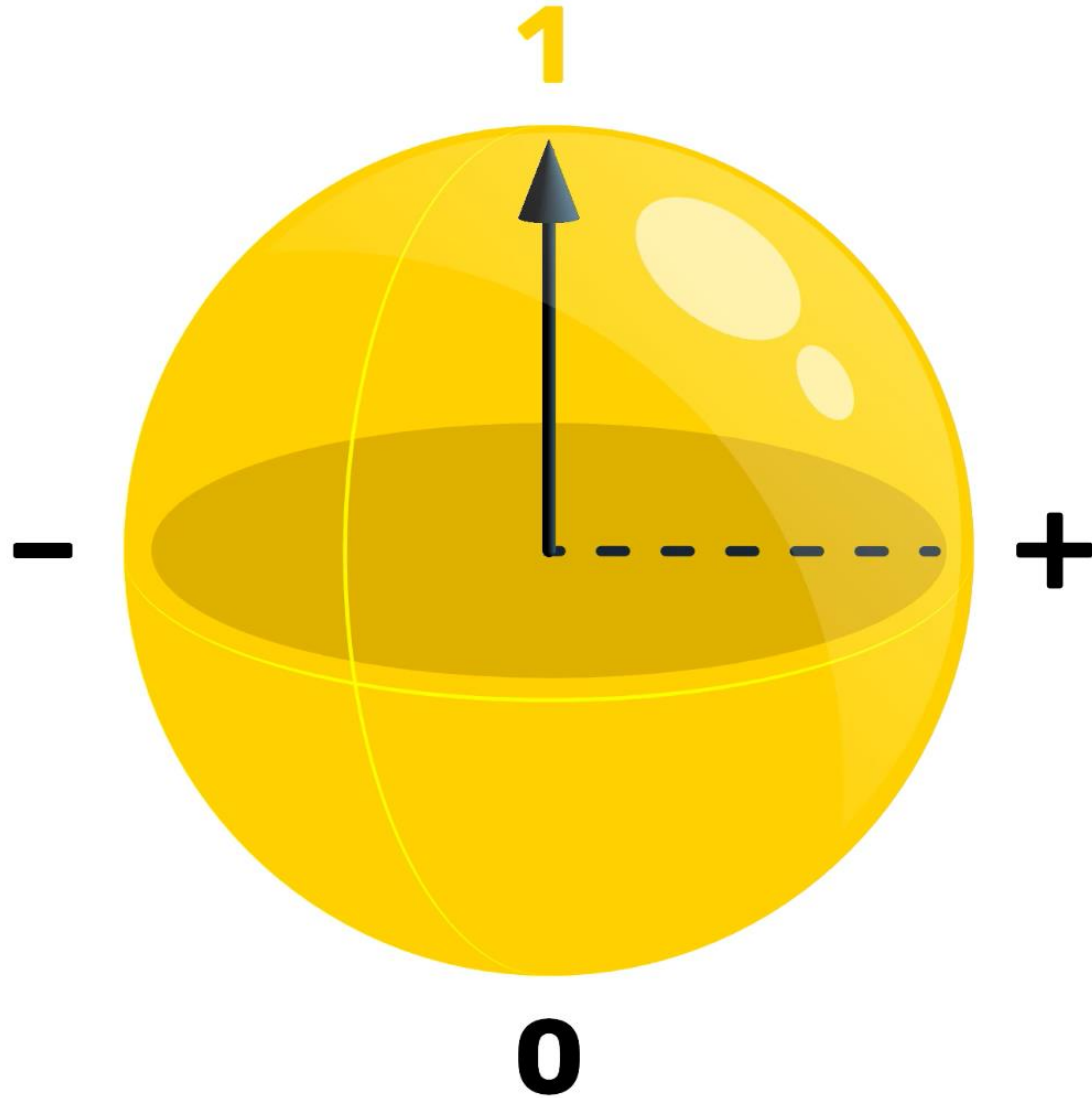
Bit

Phase



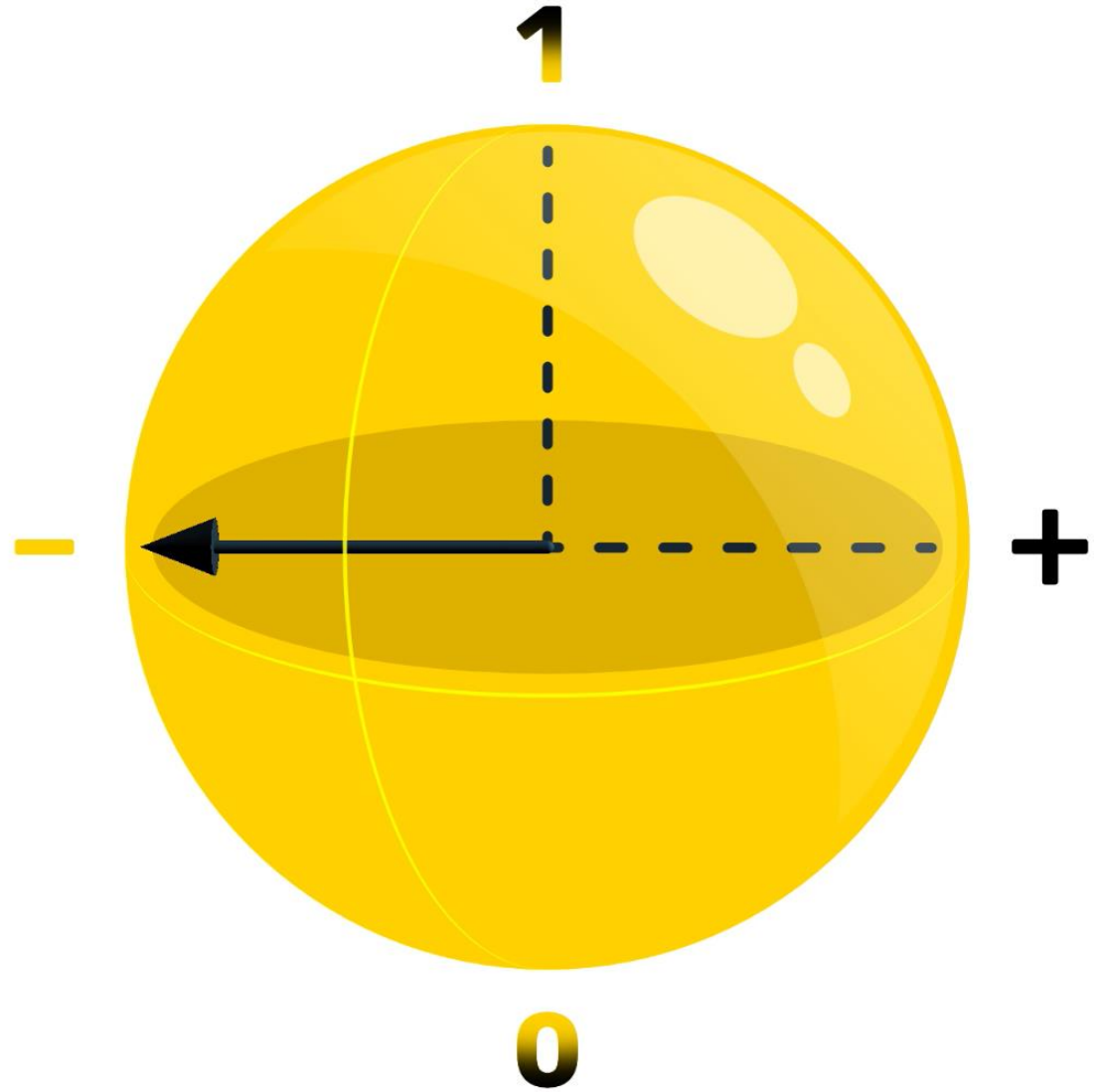


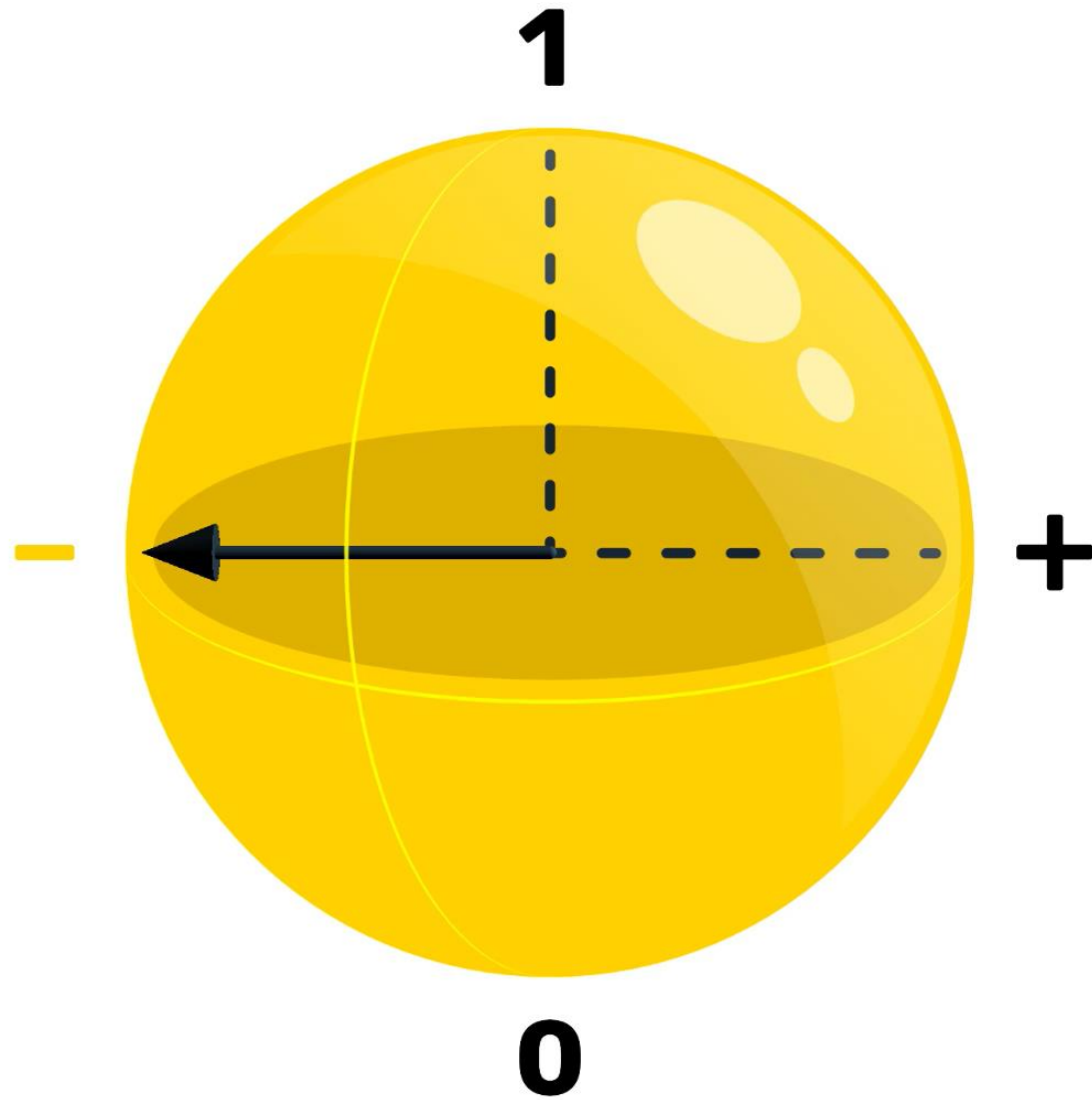
Bit-Flip





Phase-Flip

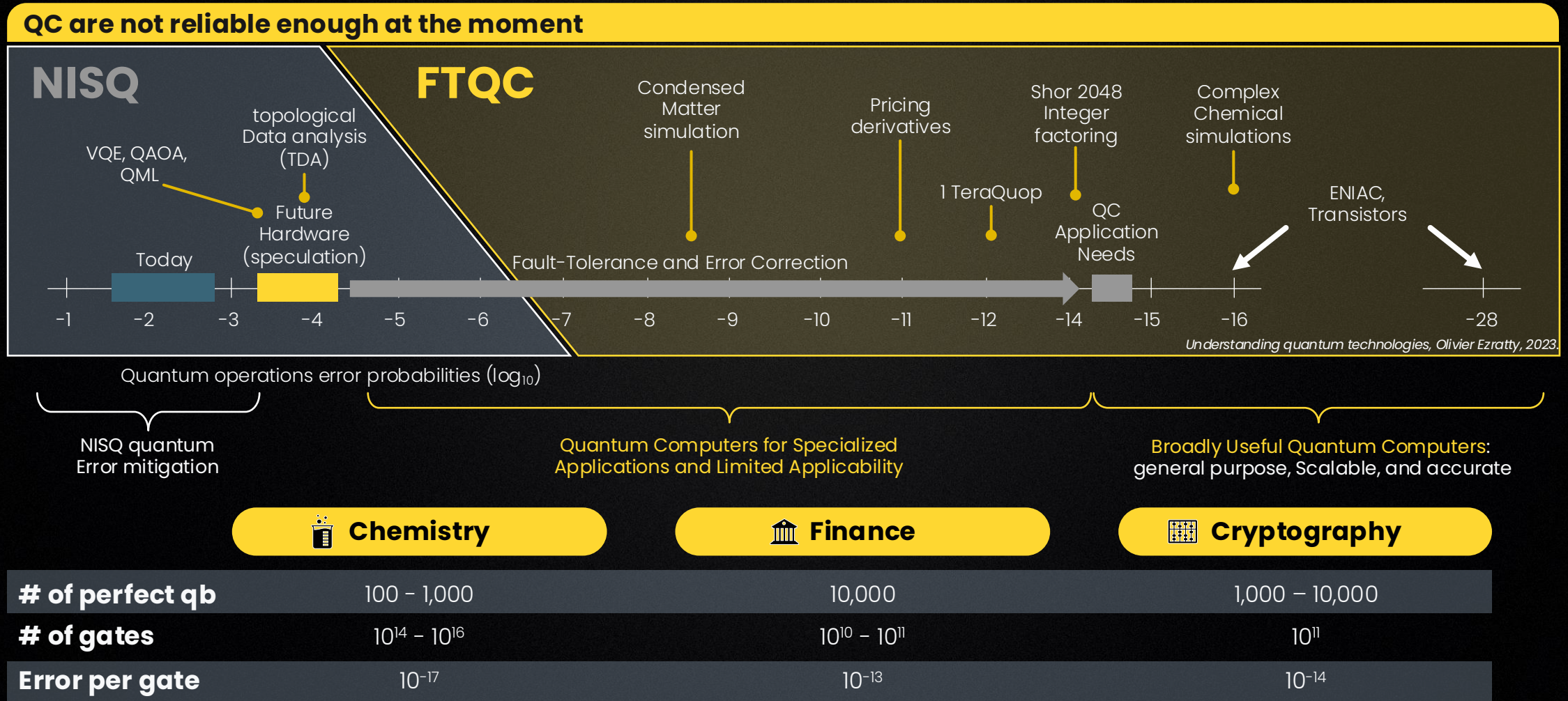






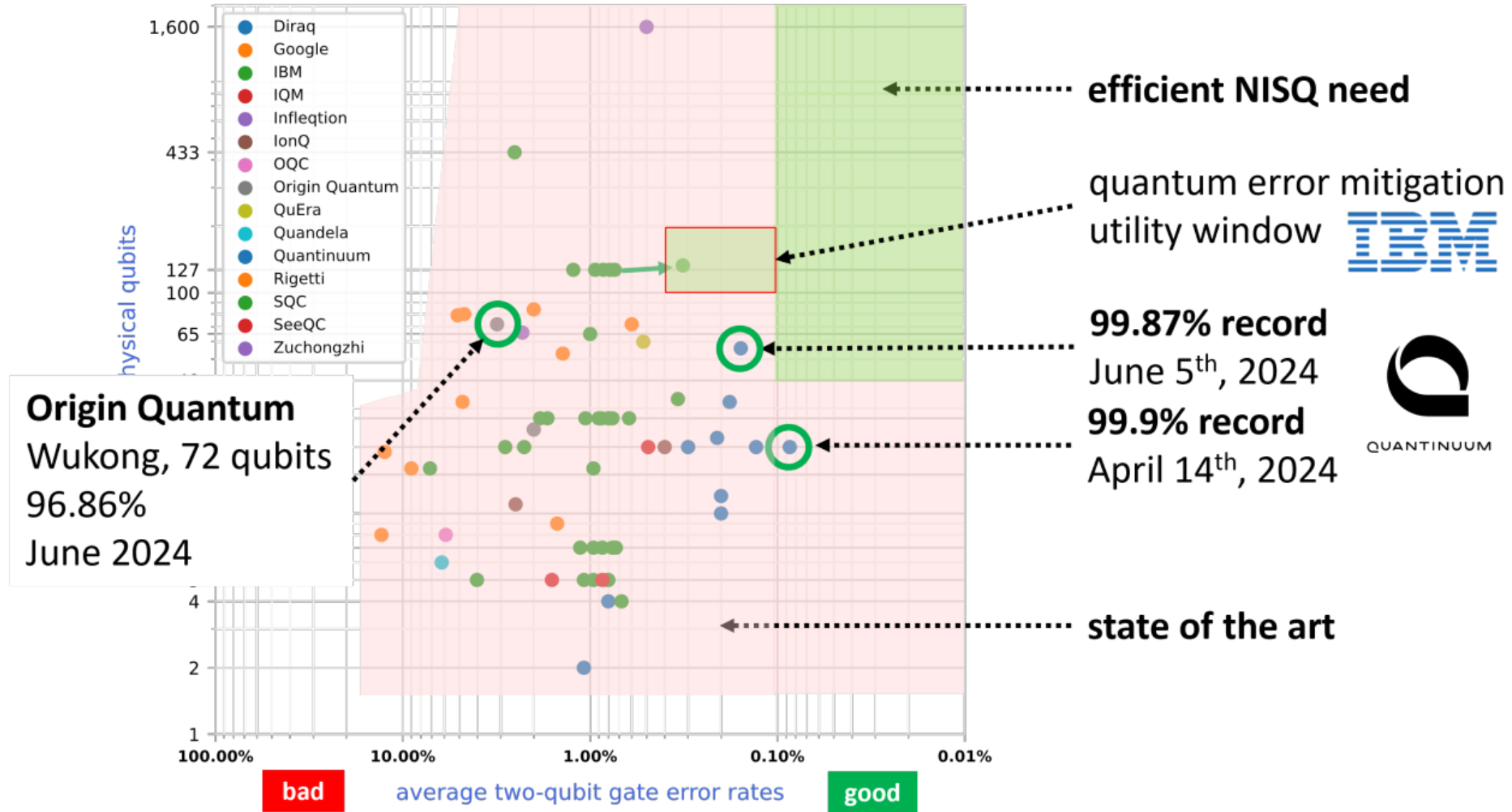
Quantum computers are still not reliable enough for their **first use case**

Very low error rates is required to unlock exponential speed-up





The specs of the current hardware...

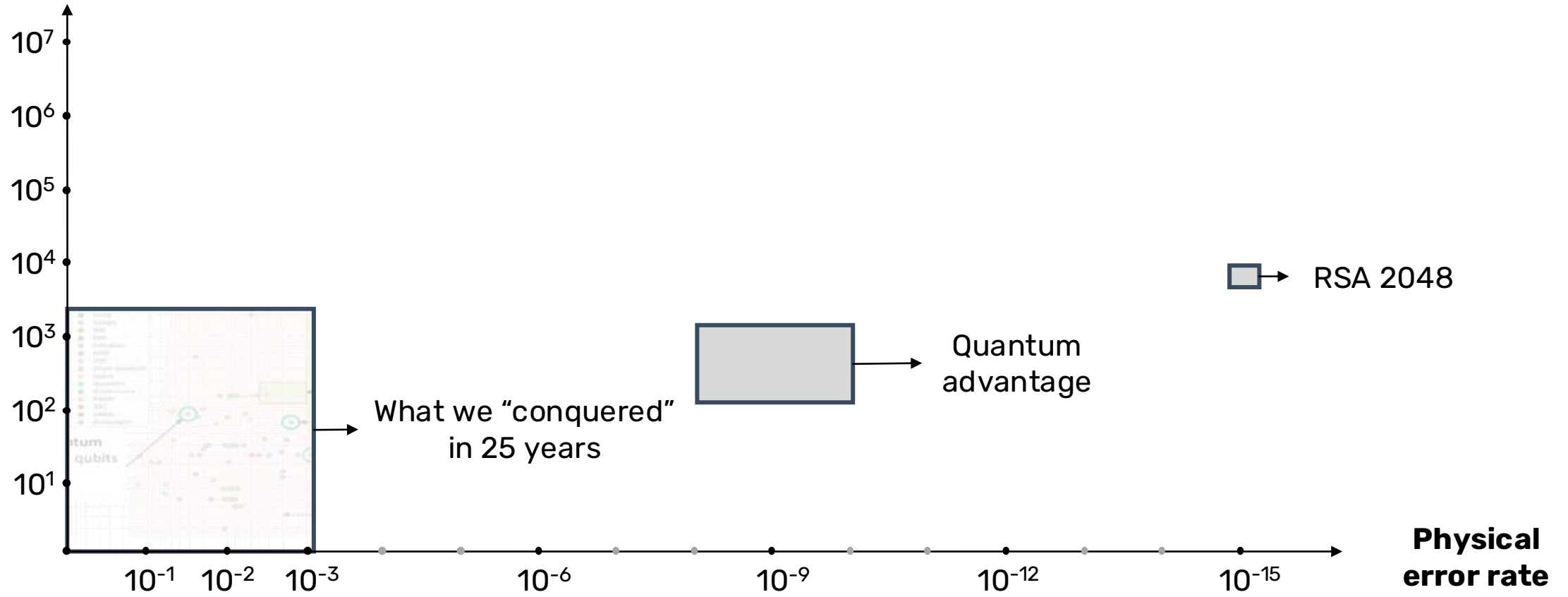


SOURCE
1. Olivier Ezratty



... and the specs we'd need to reach

of physical qubits



SOURCE

1. [Travis Scholten \(IBM\) et al., arXiv, 2024](#)



NISQ fails to fulfill its promises



From NISQ to FASQ

What we have now:

- *Noisy Intermediate-Scale Quantum (NISQ) machines.*
- Capable of performing **thousands of two-qubit operations.**
- Becoming useful for scientific exploration.
- Limited commercial value.

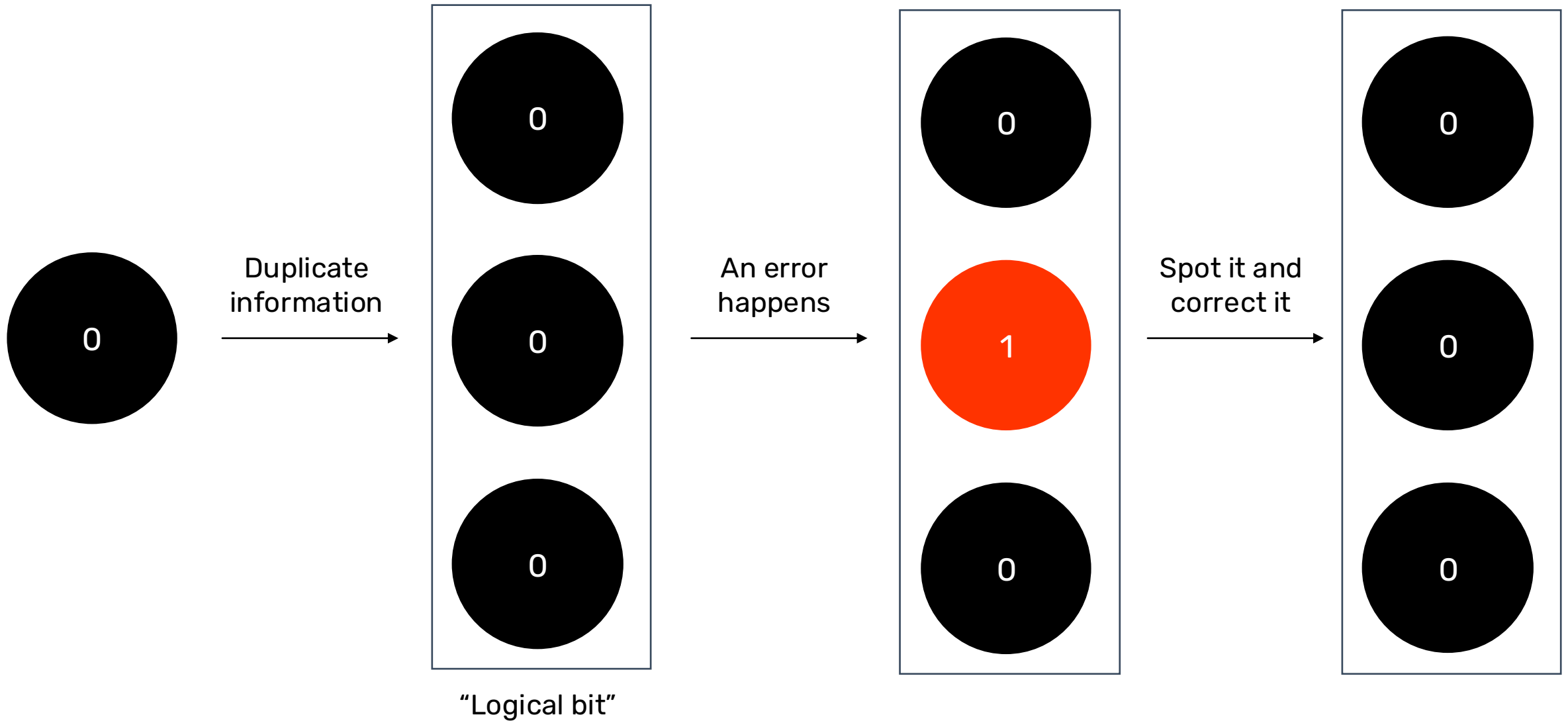
What we want to have:

- *Fault-Tolerant Application-Scale Quantum (FASQ) machines.*
- Capable of performing **billions or trillions of two-qubit operations.**
- Opening a wide variety of scientific and commercial applications.
- Need to improve error rates by many orders of magnitude!
- Quantum error correction is essential for crossing from NISQ to FASQ.

NISQ vs FTQC



	NISQ (Today)	FTQC (Future)
Qubit quality	Noisy	Error-corrected
Reliability	Limited	High
Computation length	Short	Long and scalable
Error correction	Minimal/none	Continuous
Main hardware	Physical qubit	Logical qubits
Scale	10-1000 qubits	Millions of physical qubits
Typical use	Experience & prototypes	Practical large-scale applications





“Redundant” Error Correction is The best Solution to Date to Escape the Paradox

QUANTUM ERROR CORRECTION

(In simple words)

// .01

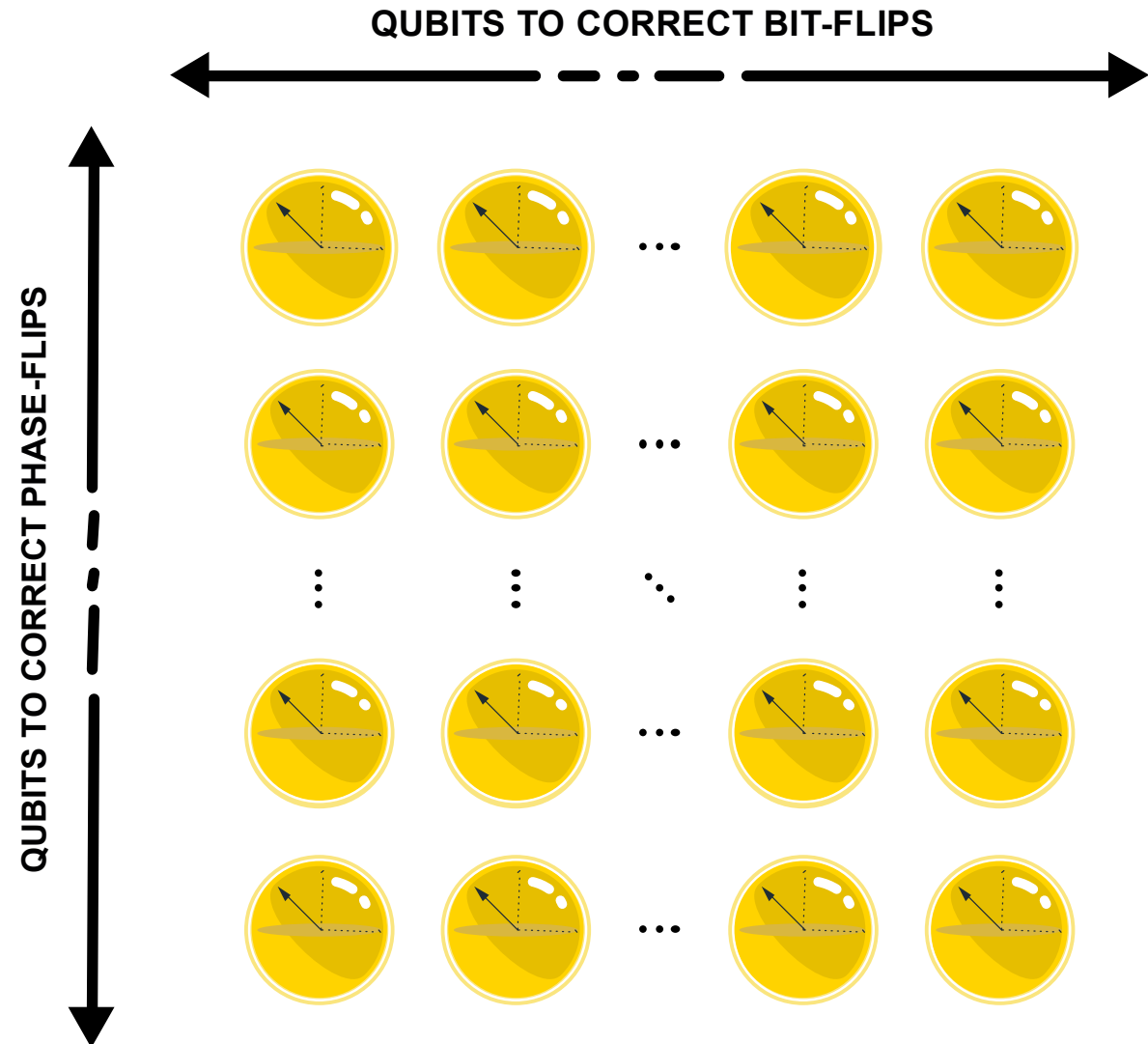
Same information within a set of qubits

// .02

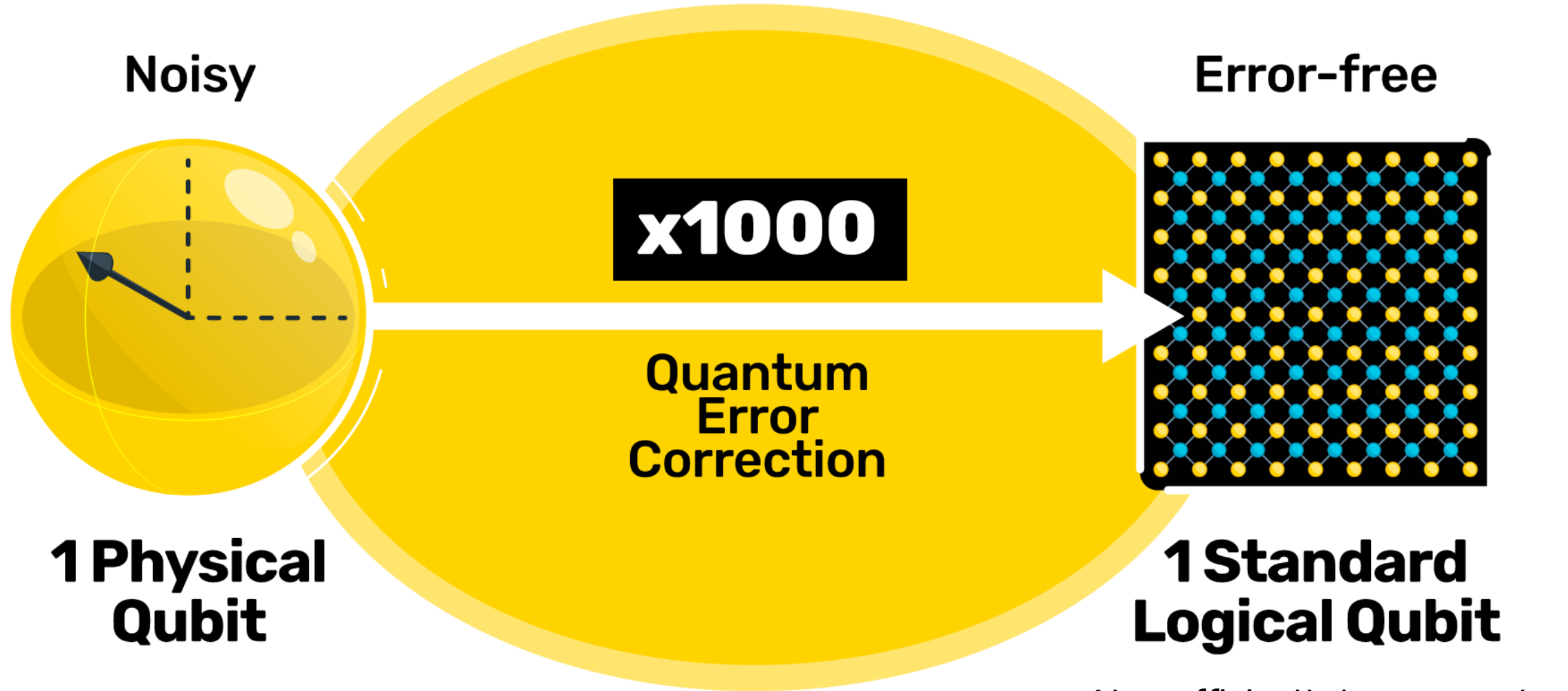
Check if the qubits in the set “agree” with each other

// .03

If a qubit is different from its neighbors, correct it



Quantum error correction is qubit-expensive...



At a sufficiently low error rate to break RSA 2048 ^{1,2}

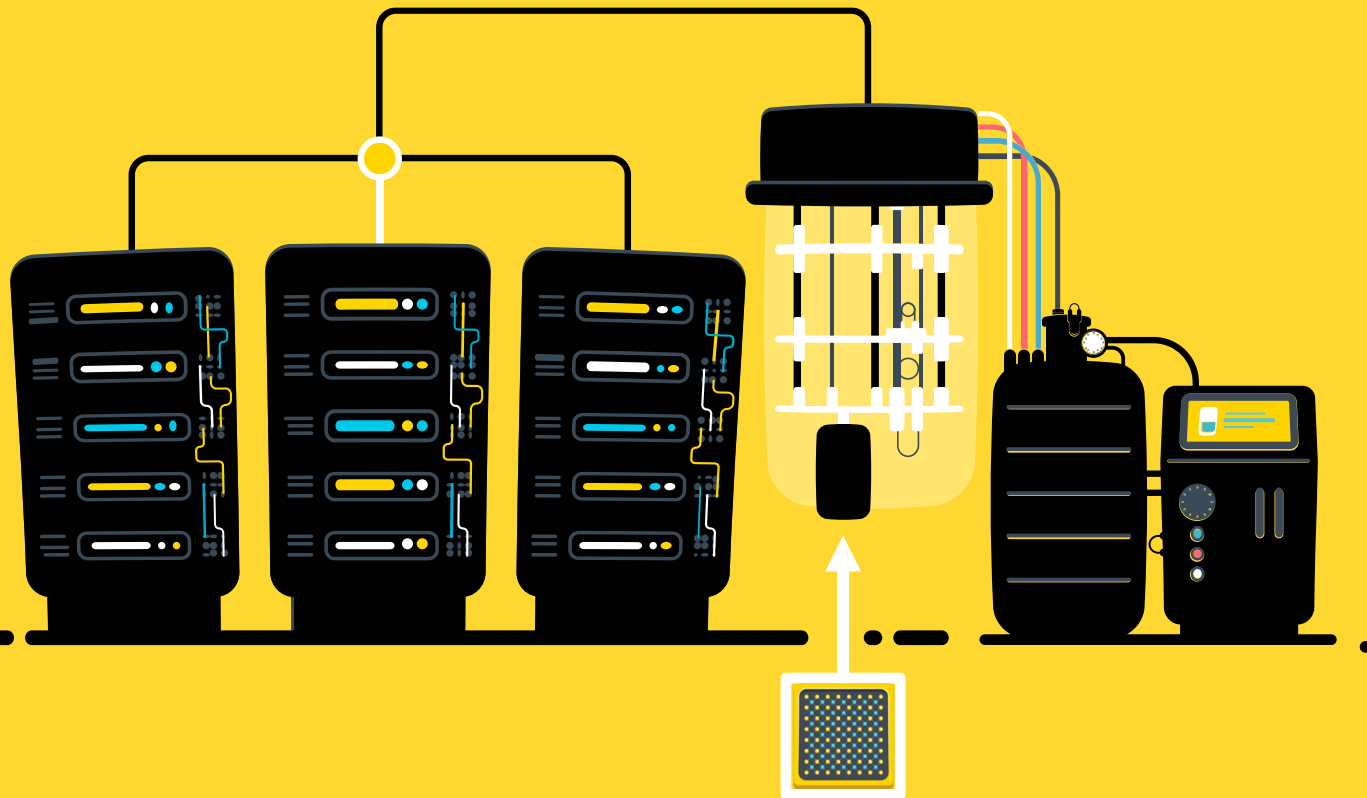
1. Craig G. et al. "How to Factor 2048 Bit RSA Integers in 8 Hours Using 20 Million Noisy Qubits." *Quantum* 5 (2021)

2. Rajiv A., et al. "Quantum Error Correction Below the Surface Code Threshold." (2024)

With Today's Technology You Would Need A Full Cryogenic System And 3 Dense Control Racks To run a Logical Qubit



~10 M\$ | ~50 kW



1 000
Physical Qubits

1
Logical Qubit

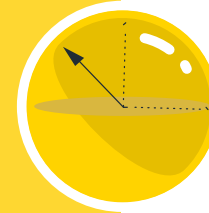
What Does it Mean for Quantum Computing at Scale?



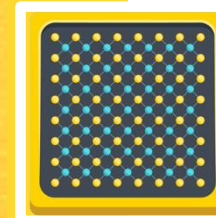
100
CRYOSTATS

→

1 B\$
1 000 kW



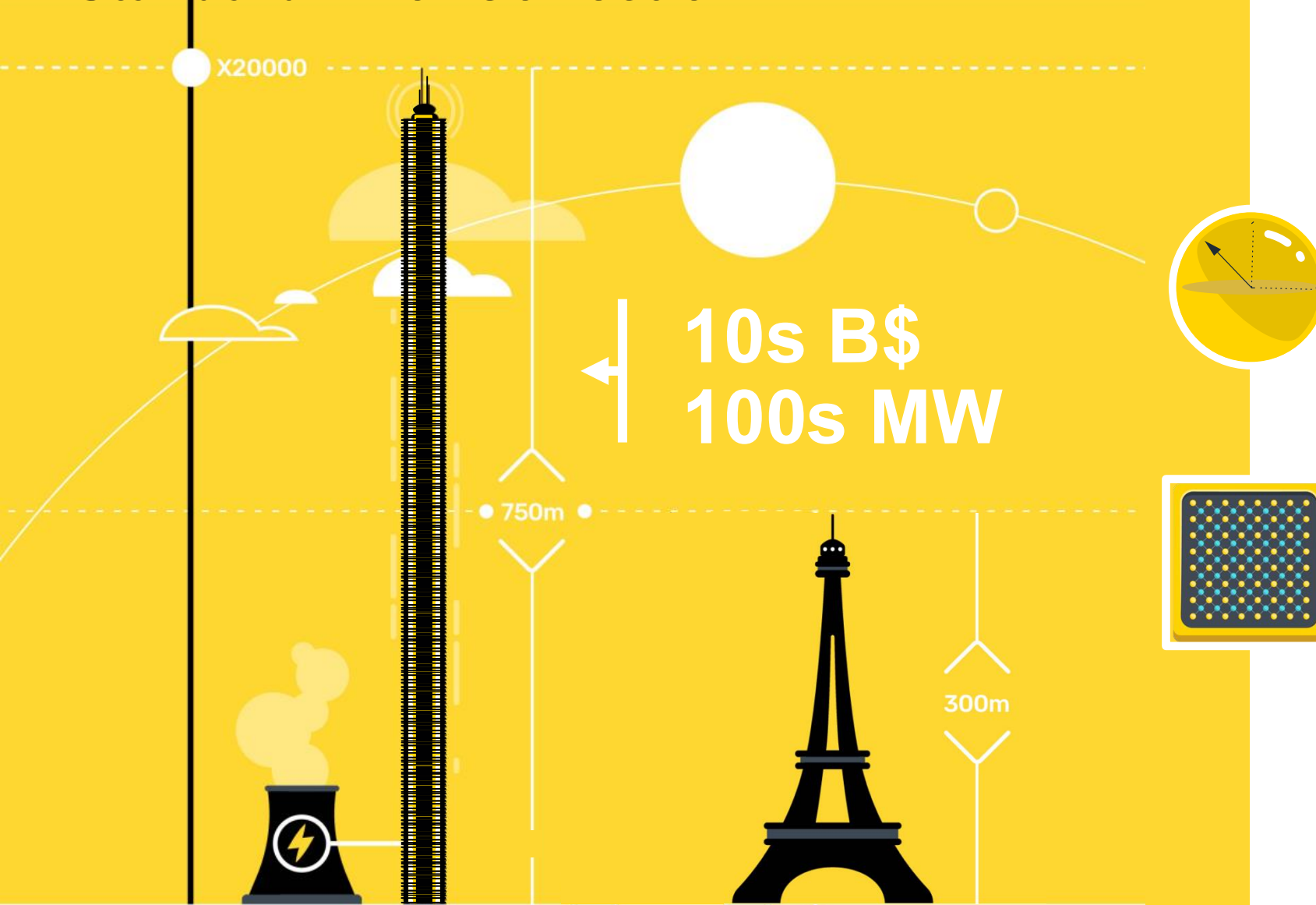
100 000
Physical Qubits



100
Logical Qubits



Run Shor's Algorithm on Standard Error Correction

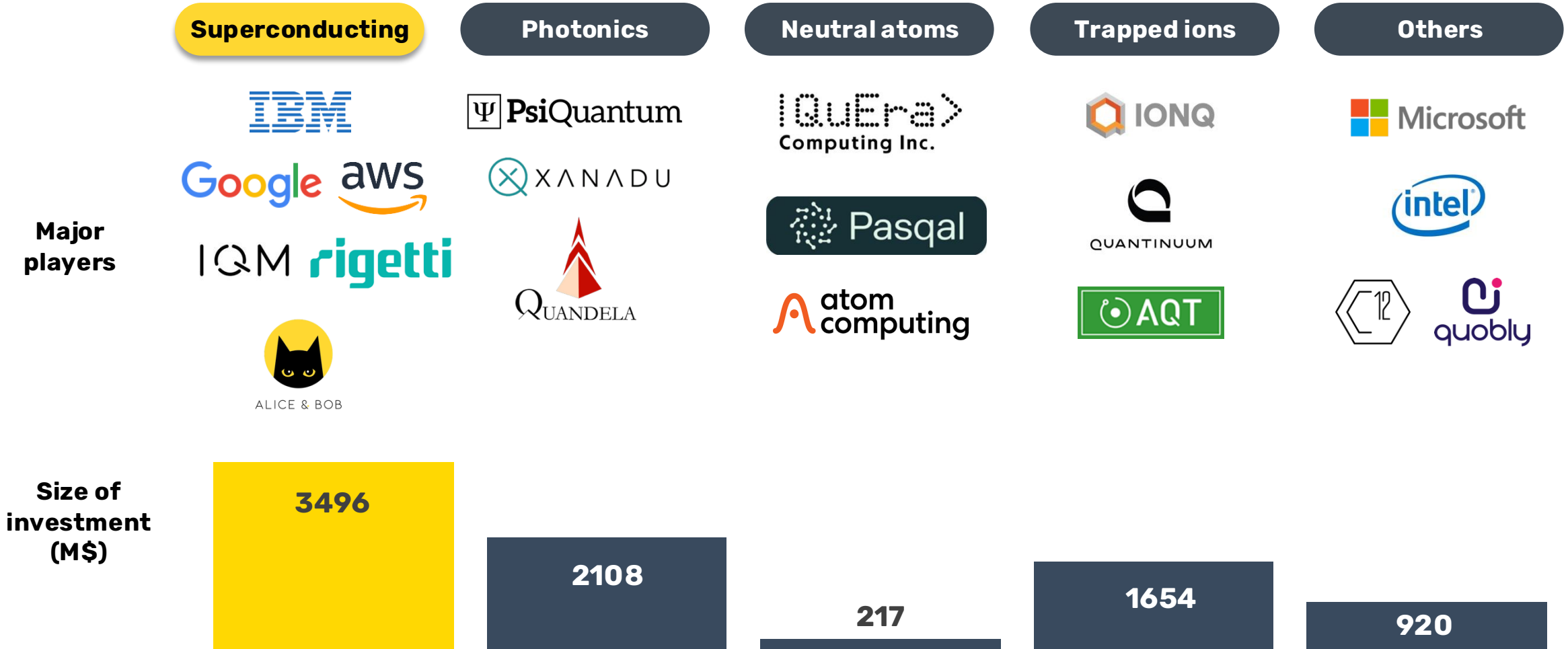


20 000 000
Physical Qubits

20 000
Logical Qubits



Ways of doing a Qubit



SOURCES

1. [McKinsey](#)
2. [Olivier Ezratty](#)

NOTE

1. Figures need to be updated with the latest 2025 investments
2. These estimations include guesses for companies whose investment figures are not public. We assumed \$500M for Google, IBM, Alibaba and AWS in superconducting qubits, \$200M for Honeywell in trapped ions and \$200M for Intel in silicon spin qubits.

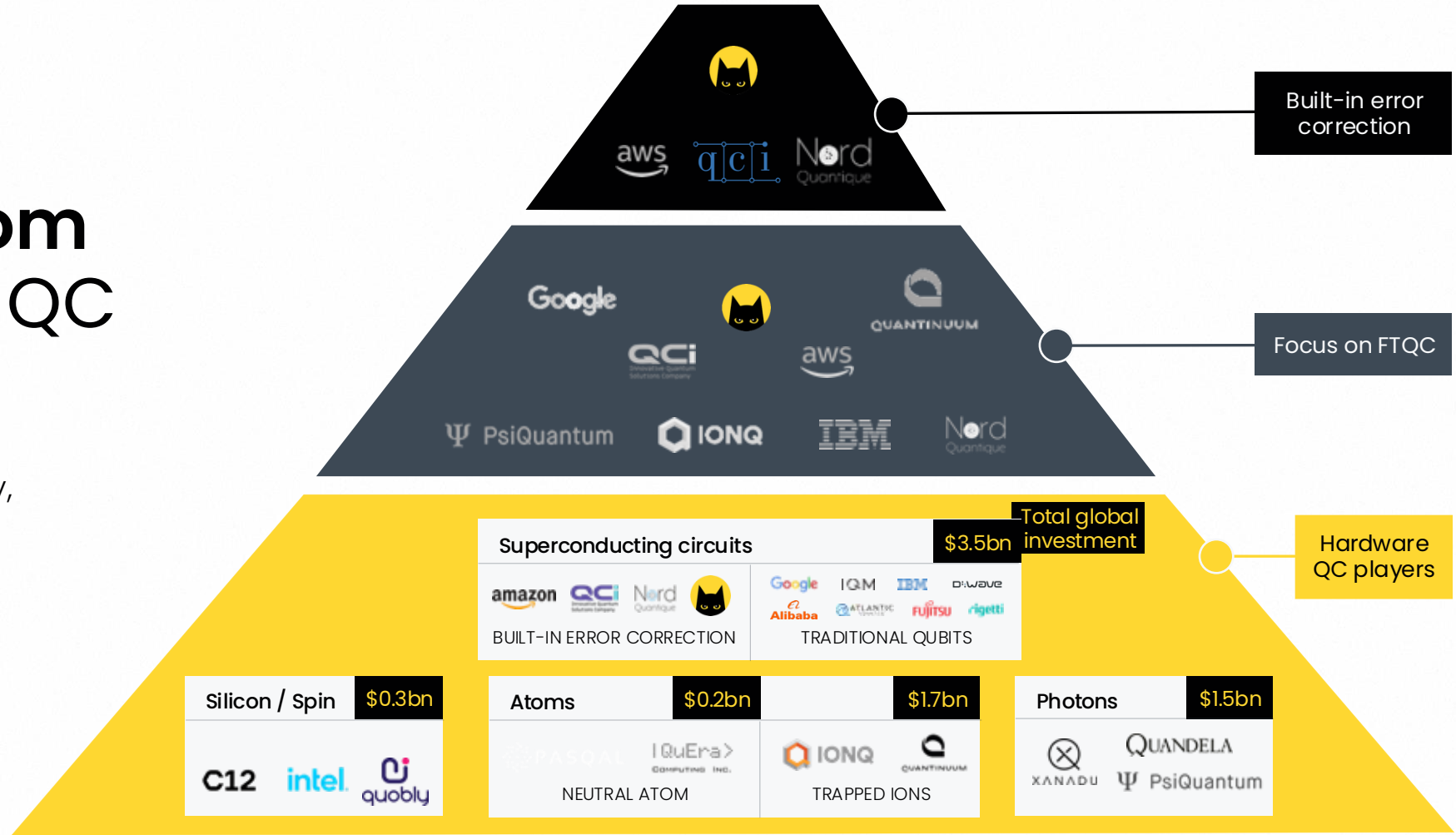


How Alice & Bob differentiates from other hardware QC actors

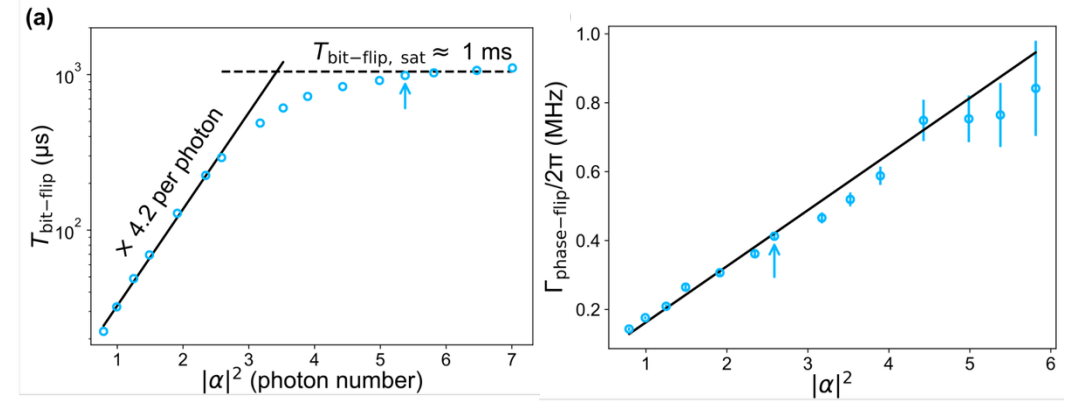
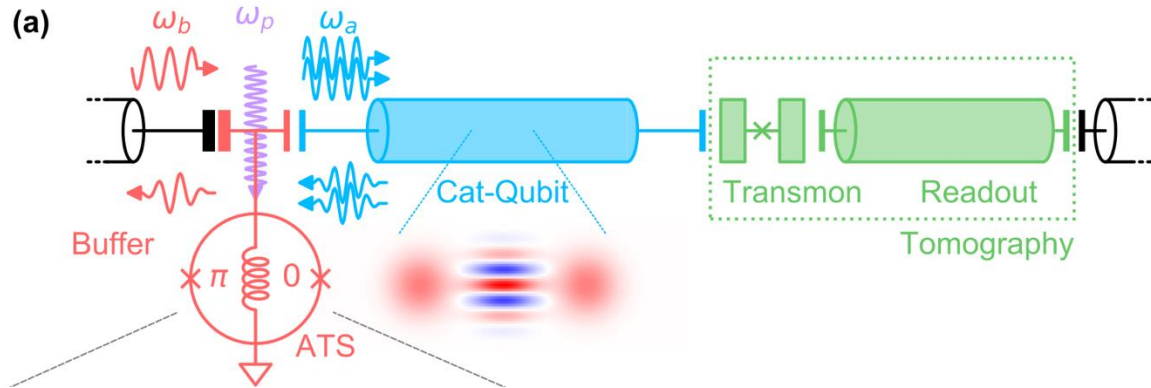
A superconductor-based technology, aiming at FTQC with our built-in error corrected qubit: the cat qubit

12x

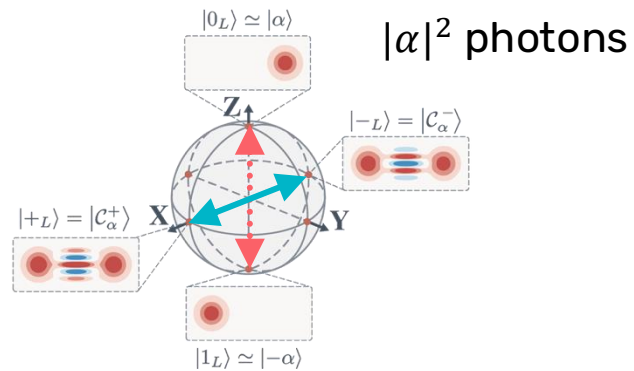
Increase in cat qubit research publications over the past 5 years



The Cat Qubit



Bit Flips: Autonomous QEC



Strong **noise biasing**:

- **Bit-flips** are suppressed **exponentially**

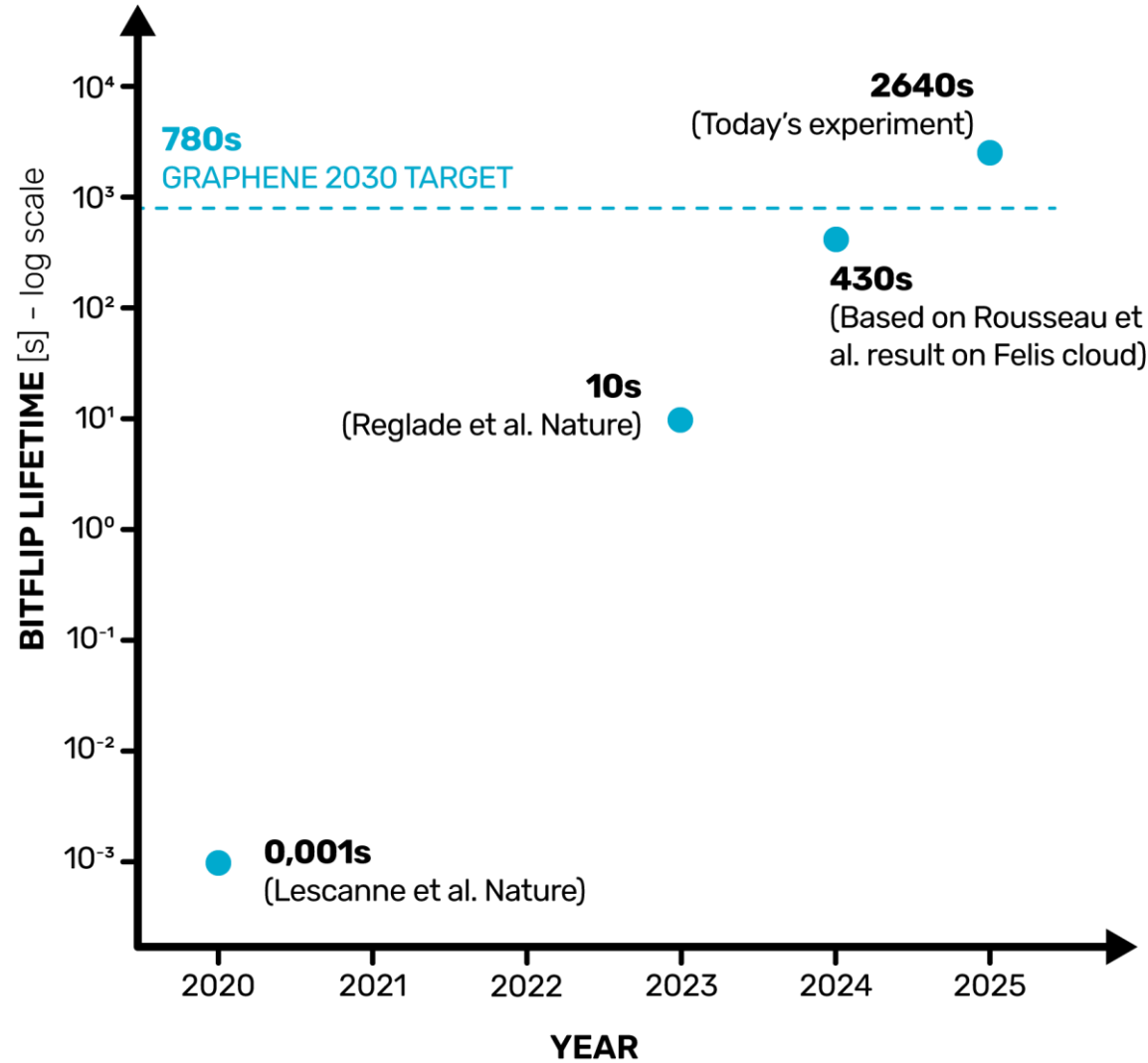
$$\Gamma_X \propto e^{-\gamma|\alpha|^2} \quad (\gamma \approx 2)$$

- **Phase-flips** increase **linearly**

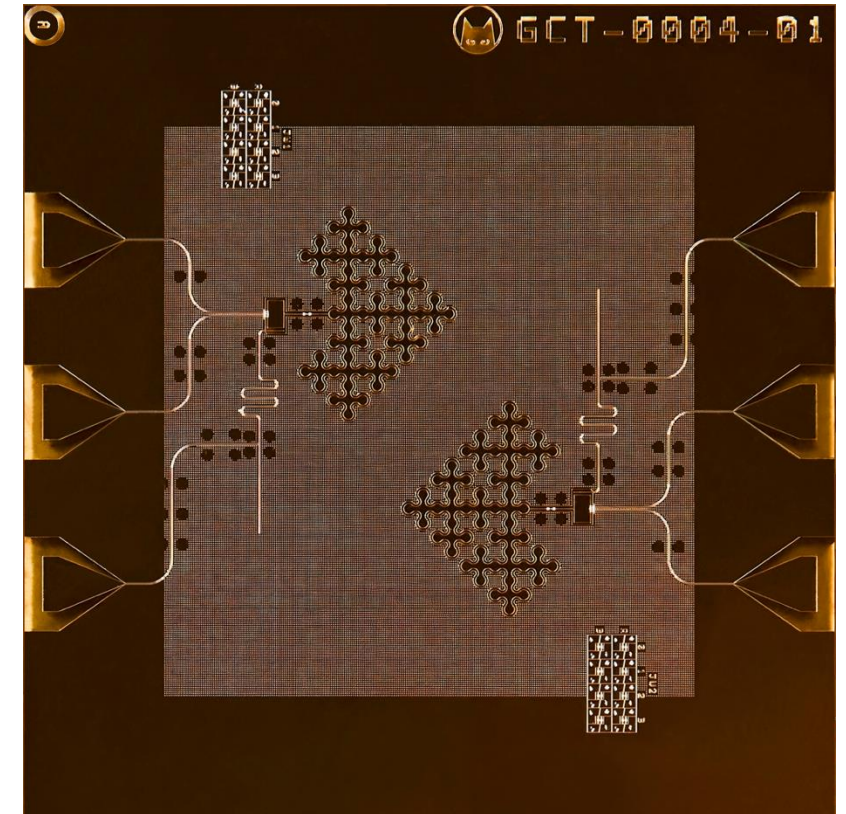
$$\Gamma_Z = 2|\alpha|^2 \kappa_1 \quad (\text{single-photon loss})$$



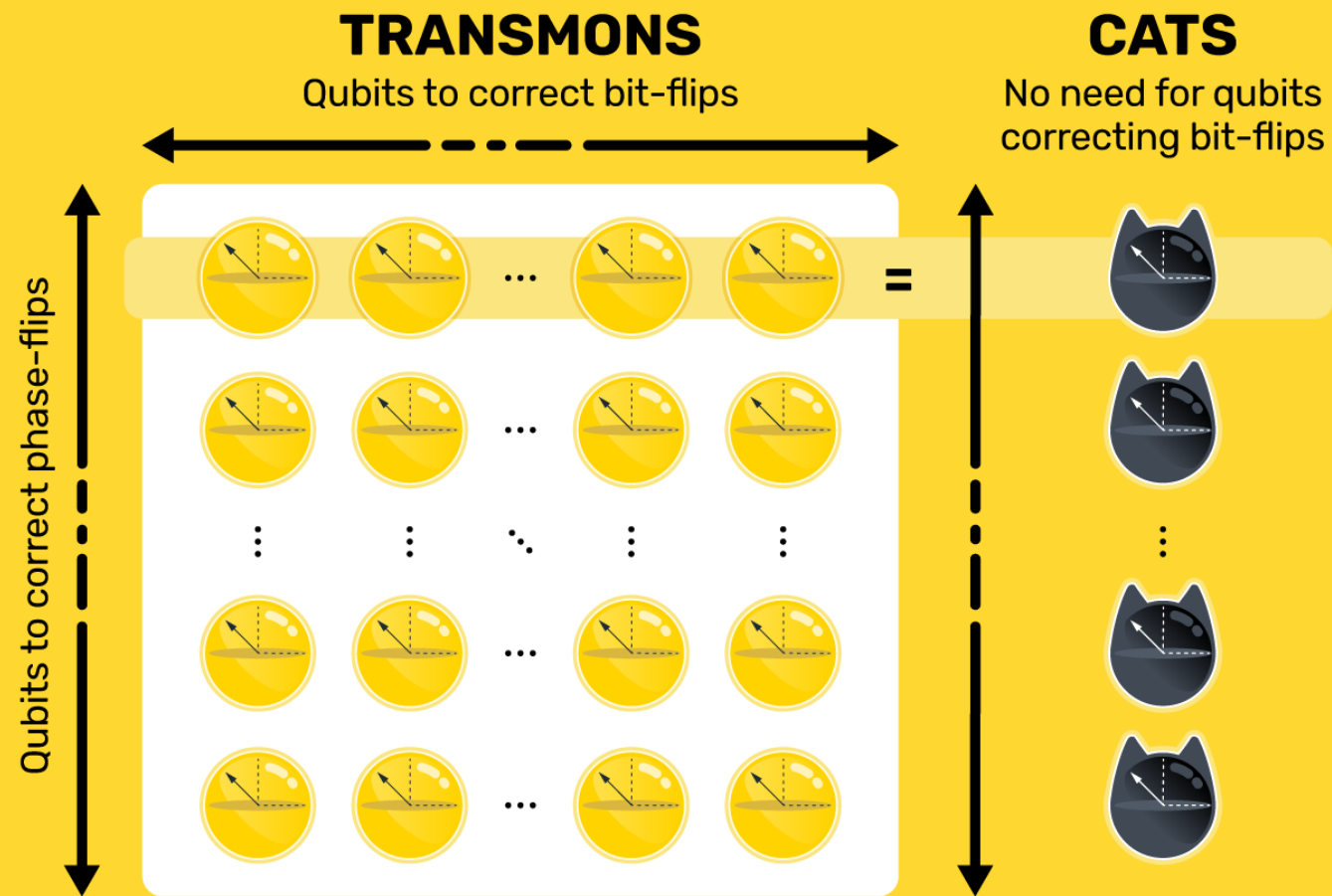
ALICE & BOB BIT-FLIP LIFETIME BY YEAR



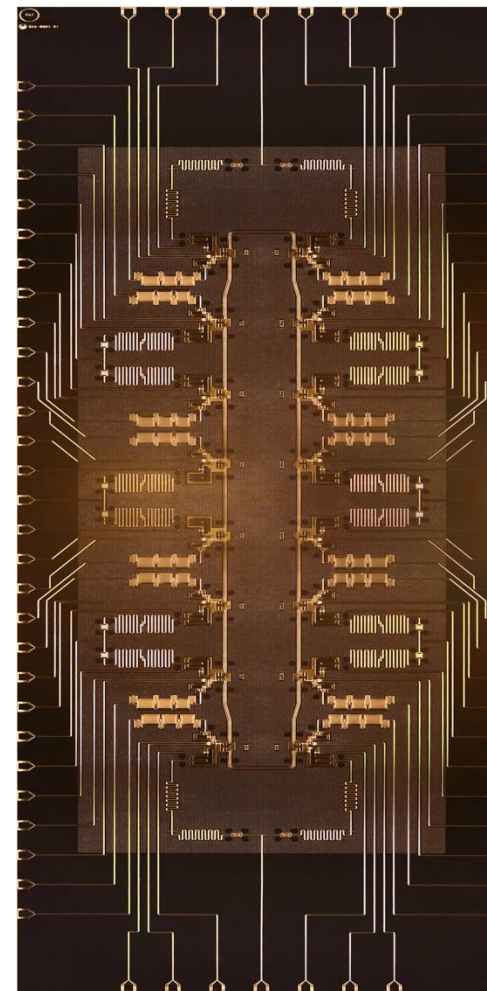
BOSON 4 SINGLE CAT-QUBIT ON THE CLOUD



With Only One Error Left, Correction Can Be One-dimensional, Only Taking Care Of Phase Flips



HELIUM, 16 CAT-QUBIT NOW IN TESTING




- MEASURE
- DATA



Low-overhead FTQC with repetition-cat code


Bit-flip suppression*

39 Transmon qubits [1]	VS	1 cat qubit  [2]
----------------------------------	----	--

(* hour-long bit flip time)

[1] Google Quantum AI et al., *Nature* 638, 920–926 (2025)
 [2] Jezouin, <https://alice-bob.com/blog/just-out-of-the-lab-a-cat-qubit-that-jumps-every-hour/> (2025)

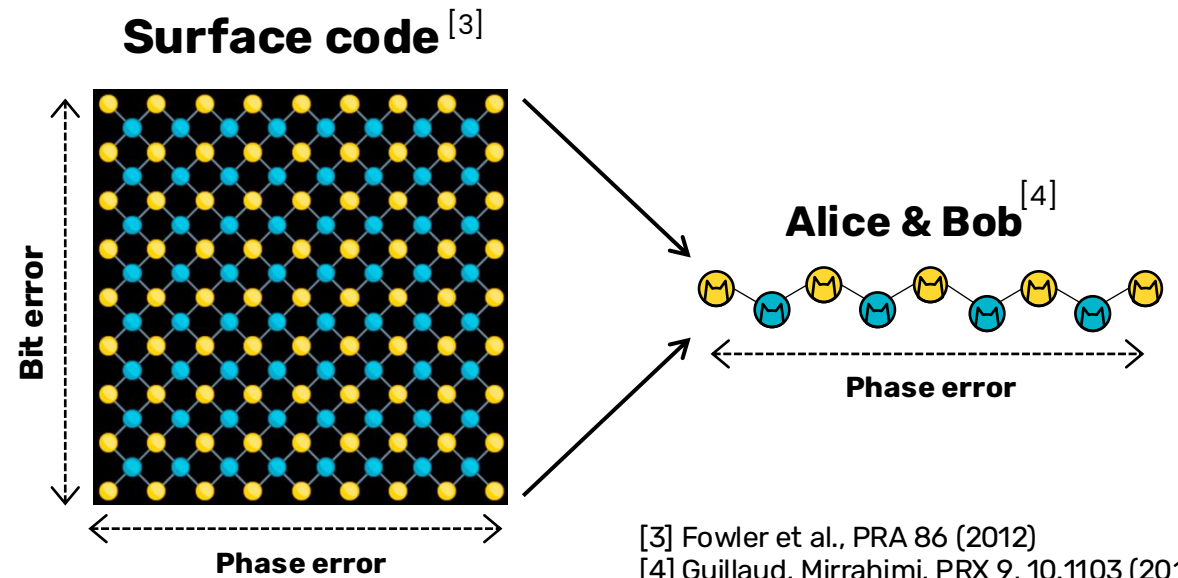
100 logical memory qubits* [5]

33,700 Transmon qubits w/ surface code	VS	2,100 cat qubits  [5] w/ repetition code
---	----	---

(* 10^{-8} logical error rate)

[5] Ruiz, Guillaud, Leverrier et al., *Nat. Commun.* (2025)

Logical qubit resources drop

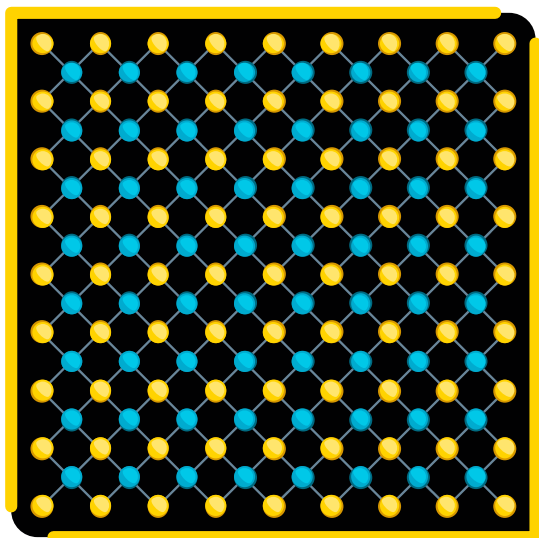


[3] Fowler et al., *PRA* 86 (2012)
 [4] Guillaud, Mirrahimi, *PRX* 9, 10.1103 (2019)

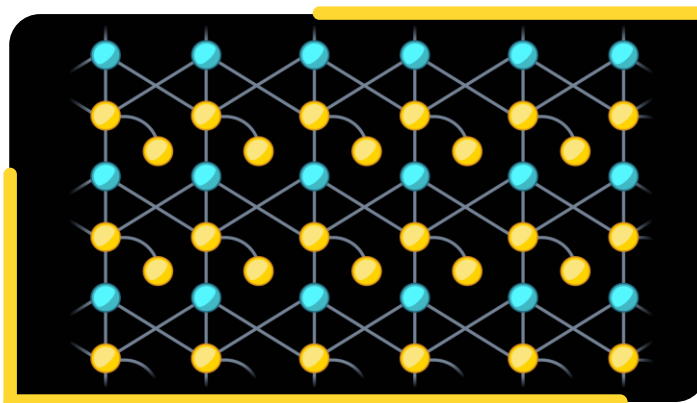
Cats Have One Dimension To Play With, Allowing For More Efficient Codes



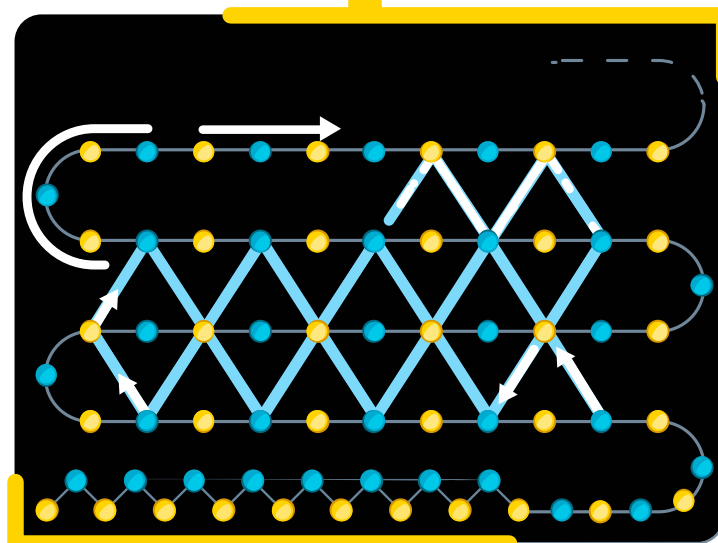
SURFACE CODE – 2D



CAT LDPC CODE – 2D¹



CAT REPETITION CODE – 1D



**EXTRA CONNECTIONS
BETWEEN DISTANT QUBITS**



**LESS PHYSICAL
QUBITS PER
LOGICAL QUBIT**



**200x
REDUCTION**

**VS STATE-OF-THE-ART
SURFACE CODE FOR SHOR**

1. Diego R., et al. "LDPC-cat codes for low-overhead quantum computing in 2D" (2024)



This is How a Useful Quantum Computer Could Become Practical Sooner

Standard approach¹

Number of physical qubits

20M

The scale of a modern small data center

LDPC+Cats²

Number of physical qubits

100k

Read the full story: Alice&Bob roadmap
alice-bob.com/whitepaper-download

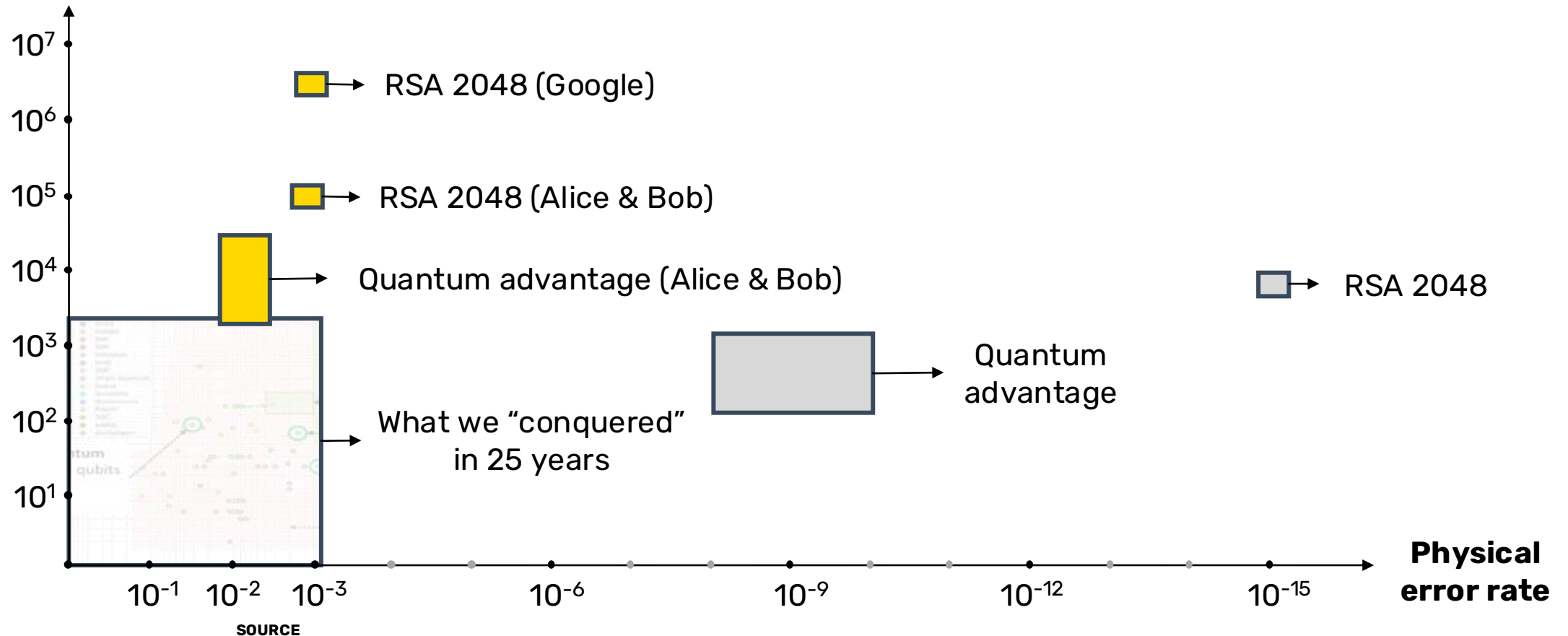
1. Craig G. et al. "How to Factor 2048 Bit RSA Integers in 8 Hours Using 20 Million Noisy Qubits." *Quantum* 5 (2021)
2. Diego R. et al. "LDPC-cat codes for low-overhead quantum computing in 2D" (2024)

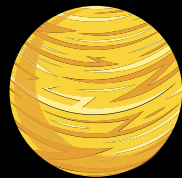


Quantum advantage is within reach thanks to cat qubits

Without logical qubits
With logical qubits

of physical qubits

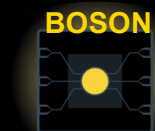




// MILESTONE 1

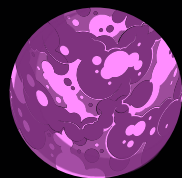
MASTER THE CAT QUBIT

2024
COMPLETE



BOSON

Cat Qubits **1**
Logical Qubits **0**



// MILESTONE 2

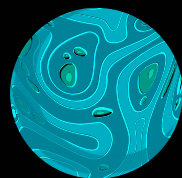
BUILD A LOGICAL QUBIT

WE ARE
HERE



HELIUM

Cat Qubits **16**
Logical Qubits **1**
Clock Speed (μ s) **1.5**
Logical Error Rate **10^{-2}**



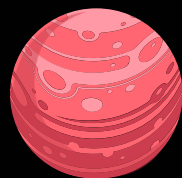
// MILESTONE 3

FAULT-TOLERANT QUANTUM COMPUTING



LITHIUM

Cat Qubits **48**
Logical Qubits **4**
Clock Speed (μ s) **0.8**
Logical Error Rate **10^{-3}**



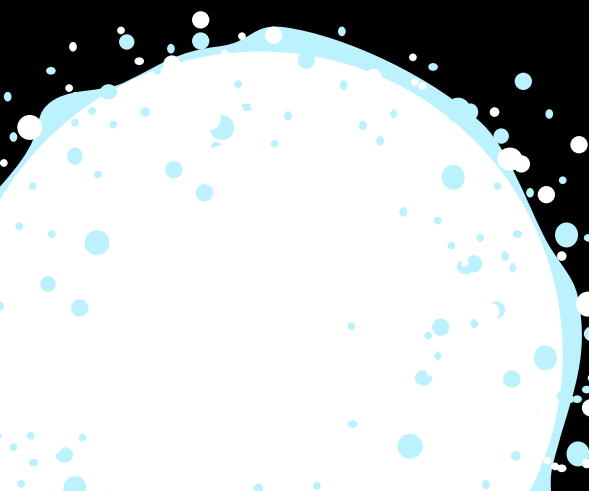
// MILESTONE 4

UNIVERSAL QUANTUM COMPUTING



BERYLLIUM

Cat Qubits **250**
Logical Qubits **5**
Clock Speed (μ s) **0.8**
Logical Error Rate **10^{-4}**



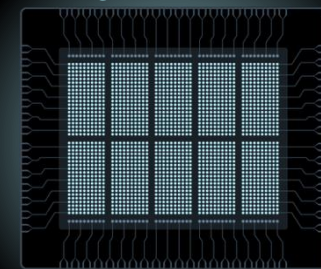
// MILESTONE 5

USEFUL QUANTUM COMPUTING

BY

2030

GRAPHENE



Cat Qubits **2 000**
Logical Qubits **100**
Clock Speed (μ s) **1**
Logical Error Rate **10^{-6}**

Focusing on **proven** computational advantage



	Problem Solved	Quantum Complexity	Classical Complexity	Computational speedup	Comments on logical qubit requirements for solving relevant-scale problems
QPE algorithm	Estimate eigenvalues of unitary operator	Polynomial for Hamiltonian simulation	Exponential in the general case for Hamiltonian simulation	Up to exponential but context-dependent	Condensed matter physics (e.g. Hubbard model with ≈ 100 logical qubits) ^[2-3] Quantum chemistry simulations with ≈ 1000 logical qubits ^[4]
Time evolution algorithm	Study dynamics of quantum systems	Polynomial	Exponential in the general case	Exponential	Evolution of system with ≈ 200 logical qubits for well-chosen problems ^[5-6]
Shor's algorithm	Integer factorization, discrete logarithm	$O((\log N)^3)$ (basic implementation)	$\exp[\tilde{O}((\log N)^{1/5})]$	Superpolynomial ²	Cryptanalysis with ≈ 1000 logical qubits ^[7-8]
HHL algorithm	Solve system of linear equations	$O(\log N)$ (under assumption)	Polynomial, typically $O(N^3)$	Exponential (under restrictive conditions)	Potential applications in data analysis or differential equations, but difficult to preserve exponential speedup in end-to-end settings ^[9-10]
Grover's algorithm	Unstructured search	$O(\sqrt{N})$	$O(N)$	Quadratic	Quadratic speedups are not enough for practical advantage ^[11-12]

2. The speedup provided by Shor's algorithm is exponential for discrete logarithms on elliptic curves, but superpolynomial for integer factorization.

Materials science simulations are the strongest candidates to benefit from eFTQC



FTQC-accelerated HPC applications

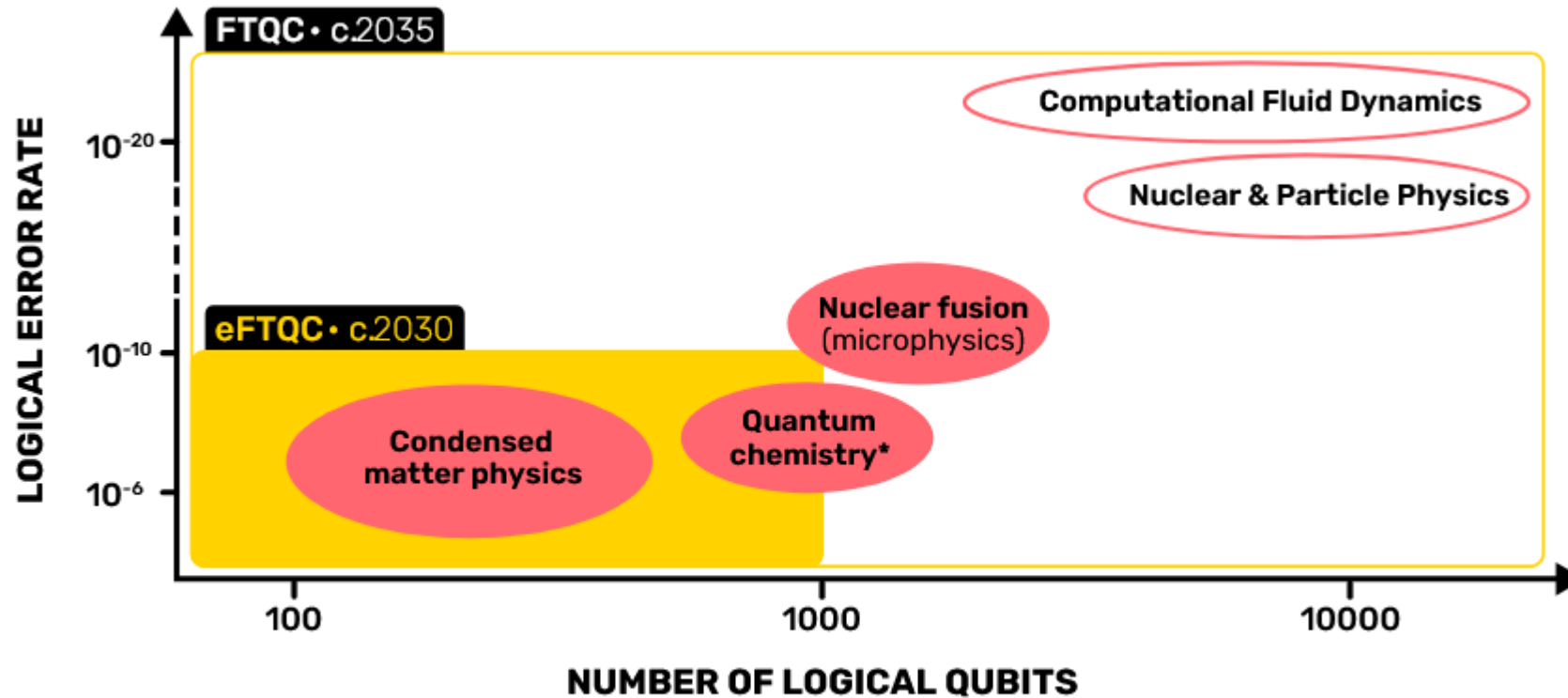
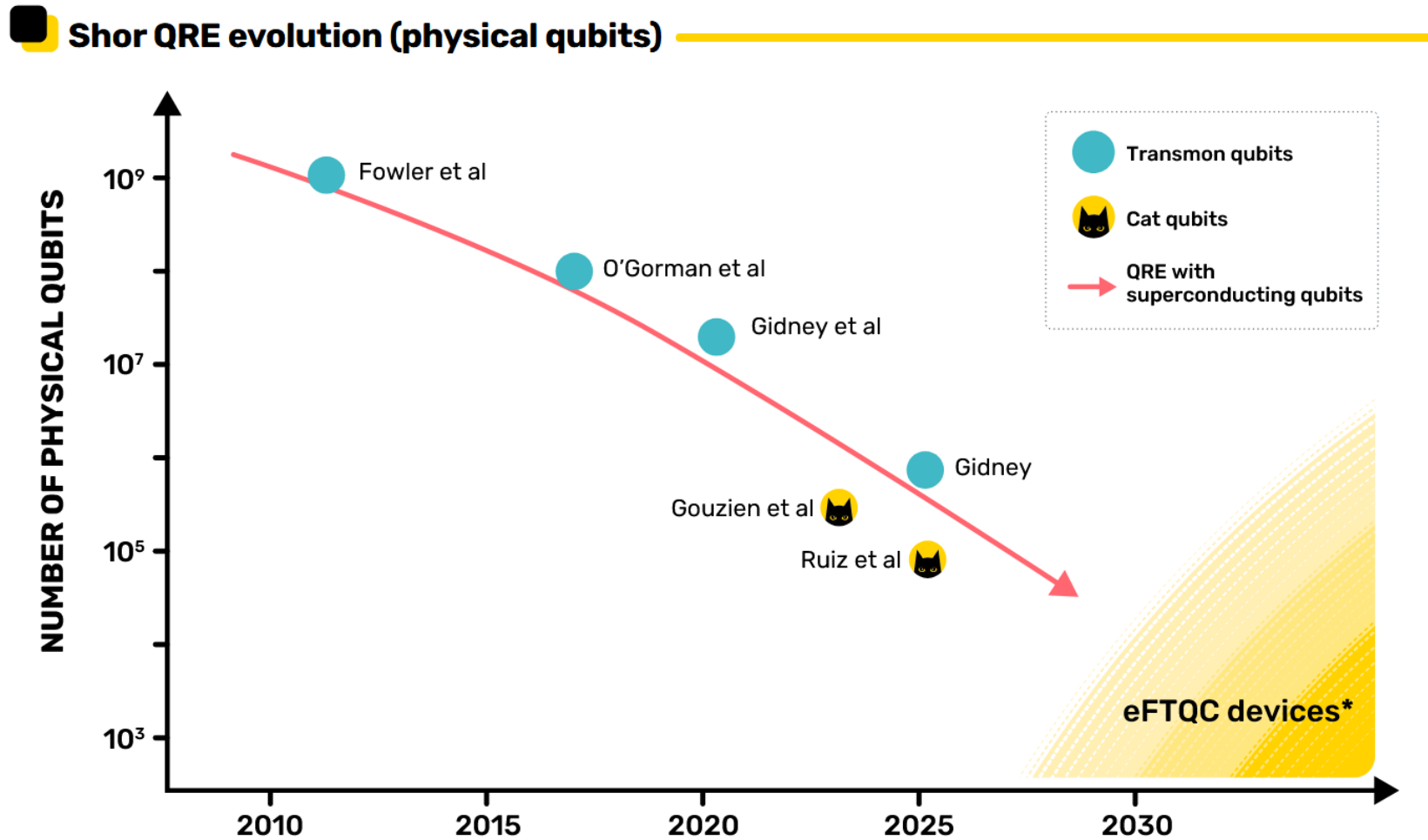


Figure 3 - FTQC acceleration roadmap: anticipated HPC application domains and timelines based on logical qubit counts and error rate thresholds. Timelines are based on quantum computing vendor roadmaps.

* Cryptanalysis using Shor's algorithm demands quantum hardware resources on par with those required for quantum chemistry workloads.

The quantum frontier is moving: more HPC applications could enter the reach of eFTQC





Resource estimations to break ECDSA with a n-bit key

Best known classical algorithms: **$O(\exp(n/2))$**

Shor's algorithm: **$O(n^3)$**

	Qubit type	Logical qubits	Physical qubits	Toffoli gates	Run time
Litinski 2023 (n=256)	Superconducting	6000	9.4×10^6	1.1×10^8	3.8 hours
Gouzien 2023 (n=256)	Cat qubits	2316	1.3×10^5	7.3×10^8	9 hours
Garn 2025 (n=233)	Photonic	3036	?	4.4×10^6	18 seconds to 4 minutes
Babbush 2026 (n=256)	Superconducting	1450	5×10^5	7.0×10^6	9 minutes

Litinski 2023: <https://arxiv.org/abs/2306.08585>

Gouzien 2023: <https://journals.aps.org/prl/abstract/10.1103/PhysRevLett.131.040602>

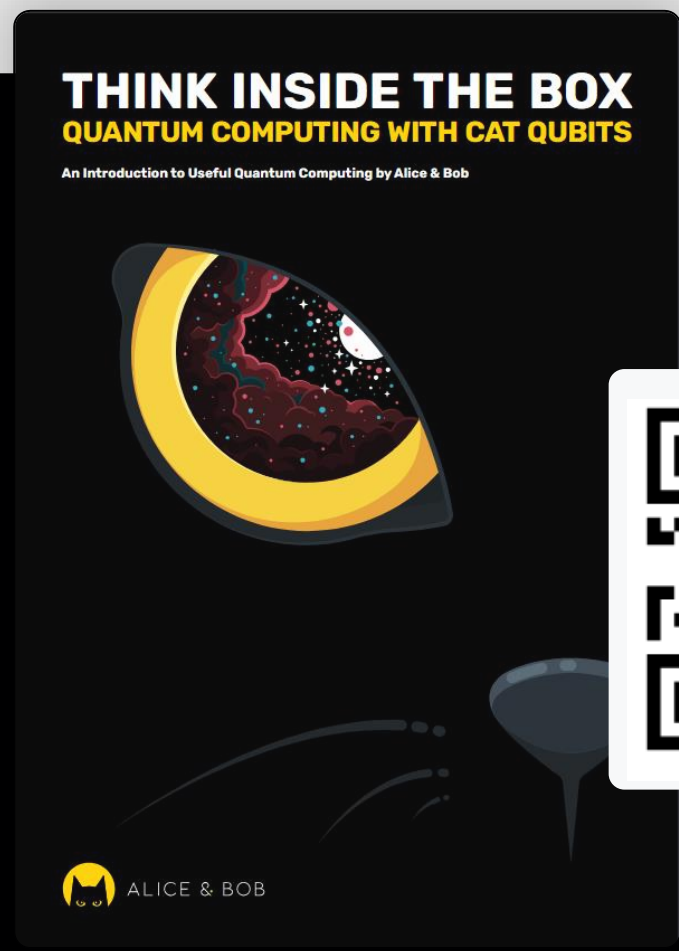
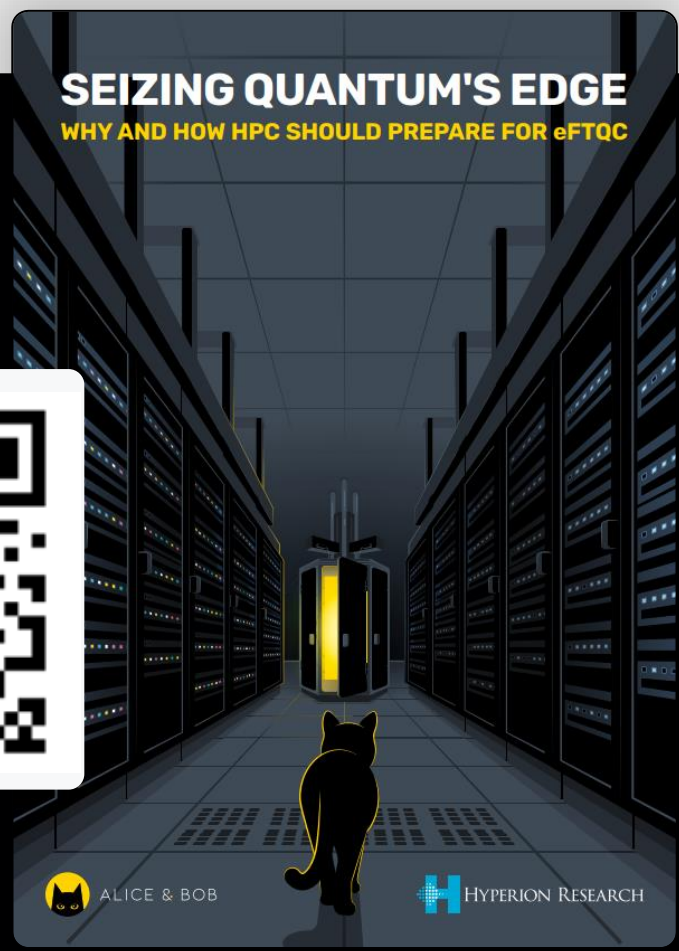
Garn 2025: <https://ieeexplore.ieee.org/document/11072281>

Babbush 2026: <https://quantumai.google/static/site-assets/downloads/cryptocurrency-whitepaper.pdf>



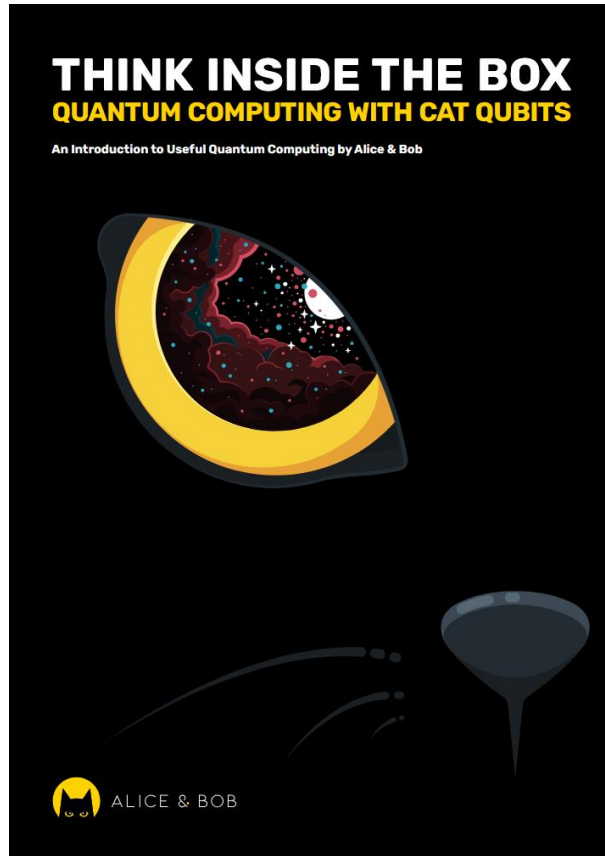
Our Latest Report

Our Roadmap

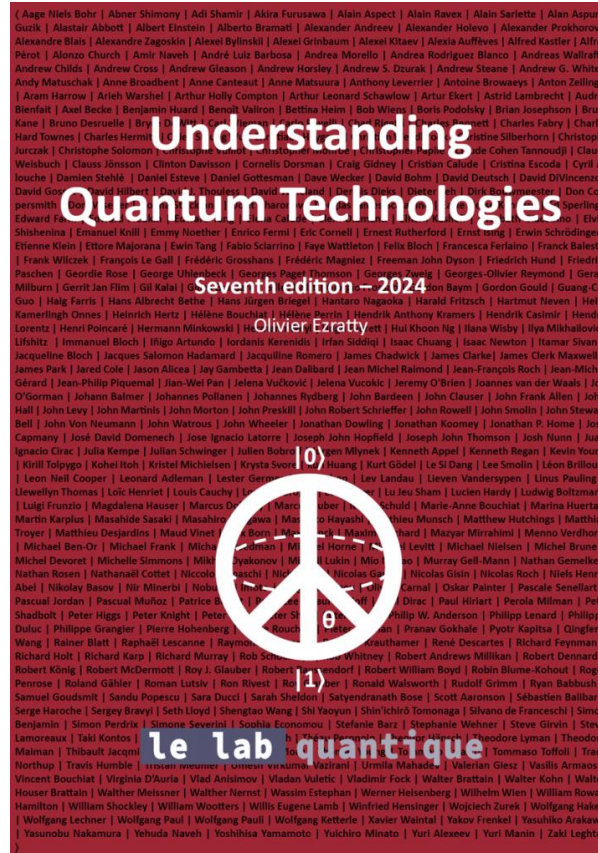




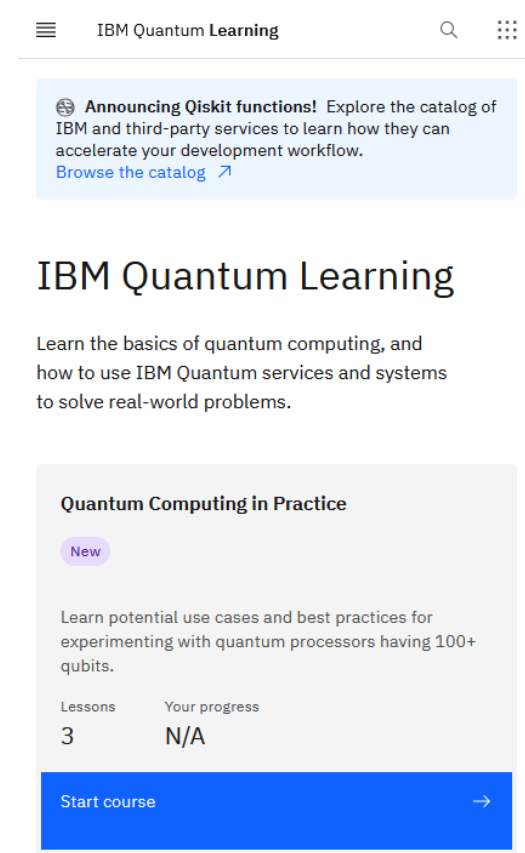
To learn more about quantum computing...



Alice & Bob's white paper



Olivier Ezratty
Understanding
Quantum Technologies



IBM Quantum Learning



Questions?